FOR REVISION DETAILS AND APPROVALS SEE ECN

Layout Verification and Archive Signoff

Objective: Completing this signoff determines the readiness of a design for mask procurement and archive. This form should be completed after final verification has been run..

Part Number (i.e. 773)	Die Revision (i.e. B)	Wafer Fab Process Flow (i.e. ABCMOS)
9887A	—	L40 DPTM
All Layer Revision: [✓]		†Partial Layer Revision: [ ]
Cadlib Process: L40 DPTM		Cadlib Version: 20

†*Note: Partial layer revisions require an XOR job to verify that no other layers were modified.*

1.0 List Instance Masters Run

Run Date: 5-17-02

2.0 Design Rule Check

Run Date: 5-17-02

Final DRC after layers have been generated and placed.

Self-intersecting, Discarded and Off-Grid errors are reported in the <jobname>.err file.
*Self-Intersecting and Discarded geometries **CANNOT** be signed off. They must be fixed.*

2.1 Non-45, Off grid or Acute angle geometries? Y [✓] N [ ] n/a [ ]
Comment/Action: AESD_SMALL_SR ESD cells, AESD_SMALL_SR_A, ESDOUT, ESD_POWER_NDIO, AESD_NSR, ESD, ESD_A

2.2 Run "listdrc" on the <jobname>.sum file and include the printout. Y [✓] N [ ] n/a [ ]
Were there any violation?; If YES, have the appropriate people sign off.
Design Engineer:
Process Engineer:
CAD Engineer: K. Neil 5/14/02
Comment/Action: TSMC Parts PPSFT35 AND NPSFT35

2.3 Were the command files MODIFIED? Y [ ] N [✓] n/a [ ]
If modified, have appropriate people sign off:
Design Engineer:
CAD Engineer:
Comment/Action:

3.0 Layout versus Schematic

Run Date: 5-17-02

Final check run after layers have been generated and placed.

3.1 Were there any CONNECTIVITY violations? Y [ ] N [✓] n/a [ ]
If yes, include copy of .lvs file and get appropriate sign off:
Comment/Action:

3.2 Were there any SIZE violations? Y [ ] N [✓] n/a [ ]
If yes, include copy of .1vs file and get appropriate sign off:
Design Engineer:_____
CAD Engineer:_____
Comment/Action:_____

3.3 Has NETLIST been MODIFIED? Y [ ] N [✓] n/a [ ]
If modified, have appropriate people sign off:
Design Engineer:_____
CAD Engineer:_____
Comment/Action:_____

4.0 XOR - required on partial revisions. Run Date: _____

4.1 Was XOR run on old and new gin files? Y [ ] N [ ] n/a [✓]
Run "listdrc" on the <jobname>.sum file and include printout.

5.0 Assembly Check

5.1 Were the guidelines contained in ADI-0017 (Assembly Design Rules) followed during design and layout? Y [✓] N [ ] n/a [ ]
Note: If the answer is no, please contact Assembly Engineering to arrange a design review.
Comment/Action:_____

5.2 Was ADI Bond used to optimize bond pad placement? Y [ ] N [✓] n/a [ ]
Comment/Action:_____

6.0 Archive Prep

6.1 Was archive tar file created? Y [ ] N [ ] n/a [ ]

Design/Layout is responsible for creating a compressed tar file of the design database for archiving. The tar file should include:

- All Cadence libraries referenced by the design (Excluding CADLIB supplied process libraries and standard cell libraries)
- All TC schematics used for simulation and verification.
- Verilog/Synopsis files.
- Verification directories, these are normally located in your project directory.

There is a skill routine "Create Archive Script" available to help create an archive tar file. The skill routine will write a unix script that will create a compressed tar file when run by the user.

7.0 Signature Approval

Assembly Engineer: _____ Date:_____
Layout Engineer: _____ Date:_____
Design Engineer: _____ Date:_____
Design Manager: _____ Date:_____
CAD Engineer: Doug K Weid Date: 5/14/02

ANALOG DEVICES

Foundry Mask Engineering

Product Definition | Die Finish | Mask Procurement | QA

Database - Database Detail & Status | Stepping Pitch | Review & Submit

Foundry: TSMC

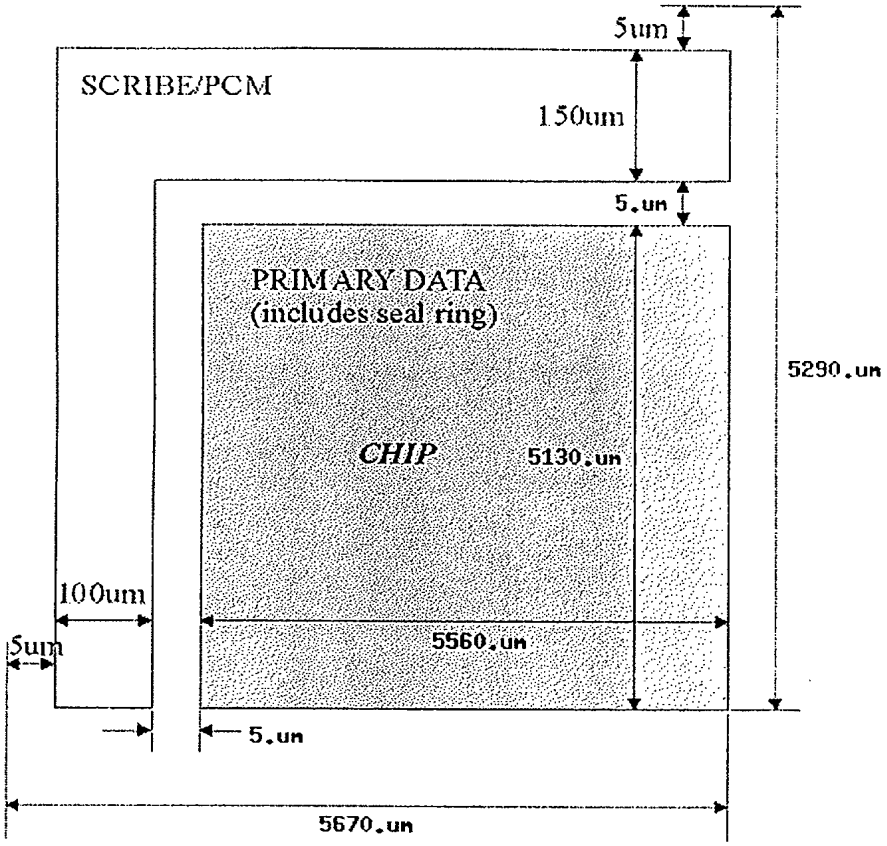
ECN	Device Name	Manufacturing
Date: 04/30/2002 **Originator:** Sandra Ireland **Number:** TOD 1378	**Foundry:** TMG285 **Rev:** A **ADI:** AD9887A **Rev:** -	**Process:** 0.35C2P3M33.00 **FAB:** FABWT(Wafertech)

Please review the following information. If you need to make modifications use the "Back" button to return to the previous page. Otherwise, press the "Complete Die Finish" button located at the bottom of the screen.

Database	**CADLIB or the Techfile path/name:**	L40DPTM
	Primary Cell Name:	9887a
	Primary Cell Coord. (um):	XLL = -2780 YLL = -2565 XUR = 2780 YUR = 2565
	Scribe:	~~110/160~~ 80x80

DRC Status:	Violations With CAD Sign-off
Are you using 3rd party IP/Libraries on this product?	No
Is the metal and poly density satisfied?	Yes
Are metal fuses being used?	No
Seal-Ring Status:	Seal-Ring Complete Complete-ADI Seal-Ring: (Spec/Rev:) L40DPTM
Mask Procurement Engineer:	Mohamed Mohamedi
Comments	

Chip Size (with Seal Ring)		Stepping Pitch	
X(um):	5560.	**X(um):**	5670.
Y(um):	5130.	**Y(um):**	5290.



Go Back	Submit Die Finish	Save and Quit	Return ECN

Projects/ad9887A/vanhoy/autoroute
HDCP_TMDS18 used in the 9887A

Projects/ad9887A/speed/Autoroute

eco3 control/Logic Top used in the 9887A

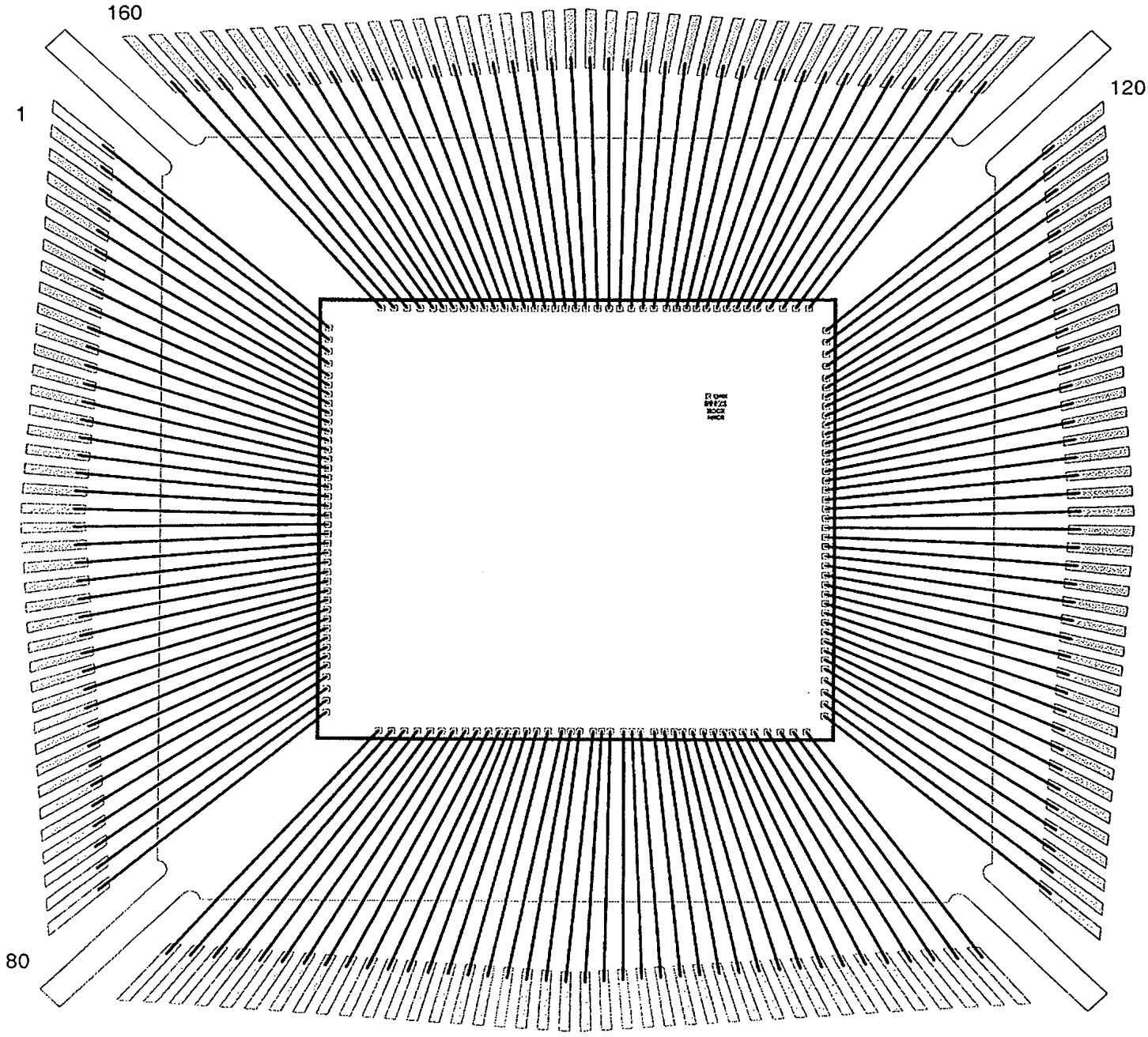
~~Autopw~~
Autoroute notes craftsman

Projects/ad9887A/Vanhoy

140_dptm_44.rules
9887.do

User: sreed(Steven.Reed) ****** UNCONTROLLED COPY ****** Date: May-22-02 09:17

Assembly Location: STATS	160L MQFP 28x28 "NULL"	



ADIBOND Ver: 5.1

ding Diagram #	Die ID	Product	Pin to bond first	Scale	CODE IDENT NO.	Bond Pad Metal Composition
	AD9887A	AD9887A	Pin 1	15x	24355	98.5%Al, 1%Si, 0.5%Cu

ANALOG DEVICES

Revision Number	Min. Passivation Opening	Die Size	Paddle Size	Foundry Number
	70 x 70µm 2.8 x 2.8 mils	5640 x 5210µm 222.0 x 205.1 mils	9000 x 9000µm 354 x 354 mils	PKG000701

Assembly Location: STATS	160L MQFP 28x28 "NULL"	

BOND WIRE STATISTICS

Product (Generic) Number(s): AD9887A

Die ID: AD9887A

Package ID: 160MQFP701

Wire Type: Gold

Package Capability: SBGA,PBGA,LQFP,MQFP

Wire Diameter: 30(1.18)

Max Allowed Wire Length: 4570(179.92)

Longest Wire: 3479(137.0)

Shortest Wire: 2707(106.6)

* ADI-0017 violations are listed.

Measurements take the form: microns (mils).

BOND PAD STATISTICS

All measurments in microns.

Min. Passivation Opening: 70x70

Min Pad Pitch: 90

ADIBOND Ver: 5.1

ling Diagram #	Die ID	Product	Pin to bond first	Scale	CODE IDENT NO.	Bond Pad Metal Composition
	AD9887A	AD9887A	Pin 1	15x	24355	98.5%Al, 1%Si, 0.5%Cu

ANALOG DEVICES	Revision Number	Min. Passivation Opening	Die Size	Paddle Size	Foundry Number
		70 x 70µm 2.8 x 2.8 mils	5640 x 5210µm 222.0 x 205.1 mils	9000 x 9000µm 354 x 354 mils	PKG000701

Assembly Location: STATS	160L MQFP 28x28 "NULL"	

Products Covered by this Document: AD9887A

COORDINATES OF BOND PAD CENTERS

1	-2705,2255	2	-2705,2115	3	-2705,1975	4	-2705,1835
5	-2705,1695	6	-2705,1585	7	-2705,1475	8	-2705,1365
9	-2705,1255	10	-2705,1145	11	-2705,1035	12	-2705,925
13	-2705,815	14	-2705,705	15	-2705,595	16	-2705,485
17	-2705,375	18	-2705,265	19	-2705,155	20	-2705,45
21	-2705,-65	22	-2705,-175	23	-2705,-285	24	-2705,-395
25	-2705,-505	26	-2705,-615	27	-2705,-725	28	-2705,-835
29	-2705,-945	30	-2705,-1055	31	-2705,-1165	32	-2705,-1275
33	-2705,-1385	34	-2705,-1495	35	-2705,-1605	36	-2705,-1715
37	-2705,-1855	38	-2705,-1995	39	-2705,-2135	40	-2705,-2275
41	-2145,-2490	42	-2005,-2490	43	-1865,-2490	44	-1725,-2490
45	-1585,-2490	46	-1460,-2490	47	-1335,-2490	48	-1210,-2490
49	-1085,-2490	50	-960,-2490	51	-840,-2490	52	-750,-2490
53	-660,-2490	54	-545,-2490	55	-435,-2490	56	-315,-2490
57	-165,-2490	58	-70,-2490	59	25,-2490	60	175,-2490
61	270,-2490	62	365,-2490	63	515,-2490	64	610,-2490
65	705,-2490	66	855,-2490	67	970,-2490	68	1080,-2490
69	1170,-2490	70	1275,-2490	71	1395,-2490	72	1505,-2490
73	1615,-2490	74	1715,-2490	75	1830,-2490	76	1955,-2490
77	2095,-2490	78	2235,-2490	79	2375,-2490	80	2515,-2490
81	2705,-2300	82	2705,-2160	83	2705,-2020	84	2705,-1880
85	2705,-1740	86	2705,-1630	87	2705,-1520	88	2705,-1410
89	2705,-1300	90	2705,-1190	91	2705,-1080	92	2705,-970
93	2705,-860	94	2705,-750	95	2705,-640	96	2705,-530
97	2705,-420	98	2705,-310	99	2705,-200	100	2705,-90
101	2705,20	102	2705,130	103	2705,240	104	2705,350
105	2705,460	106	2705,570	107	2705,680	108	2705,790
109	2705,900	110	2705,1010	111	2705,1120	112	2705,1230
113	2705,1340	114	2705,1450	115	2705,1560	116	2705,1670
117	2705,1810	118	2705,1950	119	2705,2090	120	2705,2230
121	2520,2490	122	2380,2490	123	2240,2490	124	2100,2490
125	1960,2490	126	1850,2490	127	1740,2490	128	1630,2490
129	1520,2490	130	1405,2490	131	1295,2490	132	1185,2490
133	1075,2490	134	965,2490	135	825,2490	136	705,2490
137	575,2490	138	445,2490	139	325,2490	140	195,2490
141	65,2490	142	-45,2490	143	-155,2490	144	-265,2490
145	-375,2490	146	-485,2490	147	-595,2490	148	-705,2490
149	-815,2490	150	-925,2490	151	-1035,2490	152	-1145,2490
153	-1255,2490	154	-1365,2490	155	-1475,2490	156	-1585,2490
157	-1725,2490	158	-1865,2490	159	-2005,2490	160	-2145,2490

ADIBOND Ver: 5.1

ling Diagram #	Die ID	Product	Pin to bond first	Scale	CODE IDENT NO.	Bond Pad Metal Composition
	AD9887A	AD9887A	Pin 1	15x	24355	98.5%Al, 1%Si, 0.5%Cu

ANALOG DEVICES	Revision Number	Min. Passivation Opening	Die Size	Paddle Size	Foundry Number
		70 x 70µm 2.8 x 2.8 mils	5640 x 5210µm 222.0 x 205.1 mils	9000 x 9000µm 354 x 354 mils	PKG000701

```
(gremlin. net/catwoman.adseng/disk1/ftp> cd inbox
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox> ls -l 9*
-rw-r--r--   1 7204     19970312 May 14 13:18 9887a.gin
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox> !!
ls -l 9*
-rw-r--r--   1 7204     46214560 May 14 13:21 9887a.gin
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox> !!
ls -l 9*
-rw-r--r--   1 7204     62825016 May 14 13:23 9887a.gin
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox> !!
ls -l 9*
-rw-r--r--   1 7204     70236424 May 14 13:23 9887a.gin
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox> !!
ls -l 9*
-rw-r--r--   1 7204     70568808 May 14 13:24 9887a.gin
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox> !!
ls -l 9*
-rw-r--r--   1 7204     74442752 May 14 13:24 9887a.gin
(gremlin)/net/catwoman.adseng/disk1/ftp/inbox>
```

```
(gremlin,,projects/ad9887A/vanhoy/draclvs> ftp catwoman.adseng
Connected to catwoman.adseng.analog.com.
220 catwoman FTP server (SunOS 5.8) ready.
Name (catwoman.adseng:vanhoy): anonymous
331 Guest login ok, send ident as password.
Password:
230 Guest login ok, access restrictions apply.
ftp> cd inbox
250 CWD command successful.
ftp> binary
200 Type set to I.
ftp> put 9887a.gin
200 PORT command successful.
150 Binary data connection for 9887a.gin (137.71.50.129,60716).
226 Transfer complete.
local: 9887a.gin remote: 9887a.gin
74442752 bytes sent in 4.6e+02 seconds (157.66 Kbytes/s)
ftp>
```

```
*********************************************************************************
*/N*    DRACULA  ( REV. 4.7.03-2000 /   SUN-4 S5R4   /GENDATE: 29-FEB/2000  )
                 *** ( Copyright 1995, Cadence ) ***
*/N*     EXEC TIME =10:44:41       DATE =14-MAY-2002     HOSTNAME =       xkr
*********************************************************************************

 INDISK PRIMARY CELL : 9887A


 ********** LVSNET SUMMARY REPORT **********


 WEFFECT VALUE= 0.0000000


 ******* REDUCE (LAYOUT) SUMMARY REPORT *******


 ******* STATISTICS BEFORE REDUCE ****

       MOS      BJT      RES    DIODE      CAP      UND      BOX     CELL      LDD
    292450      195      961      163      663        0        0        0        0

 OPTION TO SMASH PARALLEL DEVICES IS -- ON
 OPTION TO CONSTRUCT MOS PARALLEL/SERIES STRUCTURES IS --  ON
 OPTION TO SMASH PSEUDO PARALLEL DEVICES IS --  ON
 OPTION TO FORM CMOS GATES IS --  ON
 OPTION TO EXTRACT SUBSTRATE NODES OF GATES IS -- OFF


 ******* STATISTICS AFTER REDUCE ****
       MOS      BJT      RES      INV    DIODE      CAP     SDWI     PDWI     SUPI
     58801      168      706    56627       96      571       63        0       58
      PUPI     SDW      PDW      SUP      PUP      AND       OR      AOI     NAND
       183      165      128      387      131      513     2521      384    11199
       NOR      OAI      UND      BOX     CELL      LDD     SMID     PMID   MOSCAP
      1929     1925        0        0        0        0        0        0      186
      DRAM     SRAM
         0        0

 ******* REDUCE (SCHEMATIC) SUMMARY REPORT *******


 ******* STATISTICS BEFORE REDUCE ****

       MOS      BJT      RES    DIODE      CAP      UND      BOX     CELL      LDD
    269573      191      807      115      264        0        0        0        0


 ******* STATISTICS AFTER REDUCE ****
       MOS      BJT      RES      INV    DIODE      CAP     SDWI     PDWI     SUPI
     58800      168      706    56627       96      223       63        0       58
      PUPI     SDW      PDW      SUP      PUP      AND       OR      AOI     NAND
       183      165      128      387      131      513     2521      384    11199
       NOR      OAI      UND      BOX     CELL      LDD     SMID     PMID   MOSCAP
      1929     1925        0        0        0        0        0        0      186
      DRAM     SRAM
         0        0
```



```
 ********** LVS REPORT **********

       DATE : 14-MAY-2002
       TIME : 11:12:44


       PRINTLINE      =     1000
       WPERCENT(MOS)  =    2.000 %
       LPERCENT(MOS)  =    1.000 %
       BJT EMITTER AREA CHECK: EMAPER=    5.000 %
       CAPACITOR VALUE CHECK:  CVPER=    5.000 %
       RESISTOR WIDTH CHECK:   RESWPR=   2.000 %
       RESISTOR LENGTH CHECK:  RESLPR=   2.000 %
       DIODE AREA CHECK:       DAPER=    5.000 %
       UNSPECIFIED SCHEMATICAL PARAMETERS ARE CONSIDERED AS MISMATCH
       UNSPECIFIED LAYOUT PARAMETERS ARE CONSIDERED AS MISMATCH
1      ***************************************************
       *********   CORRESPONDENCE NODE PAIRS   **********
       ***************************************************


         SCHEMATICS                LAYOUT              PAD TYPE

 AGND                  67  AGND                    1   G
 ALGND                 68  ALGND                  99   G
 ALVDD                 69  ALVDD                 678   P
 AVDD                  70  AVDD                   81   P
 DGND                  71  DGND                   14   G
 DVDD                  72  DVDD                   13   P
 OGND                  73  OGND                    6   G
 OVDD                  74  OVDD                    5   P
 PGND                  75  PGND                   27   G
 PVDD                  76  PVDD                   28   P
 SUBGND                77  SUBGND                  4   G
 green_out_b<0>        32  green_out_b<0>      12637   I
 green_out_a<6>        18  green_out_a<6>      16151   I
 green_out_a<5>        19  green_out_a<5>      16127   I
 green_out_a<4>        20  green_out_a<4>      16096   I
 green_out_a<3>        21  green_out_a<3>      16015   I
 green_out_a<2>        22  green_out_a<2>      15919   I
 green_out_a<1>        23  green_out_a<1>      15730   I
 green_out_a<0>        24  green_out_a<0>      15708   I
 green_out_b<7>        25  green_out_b<7>      15194   I
 blue_out_b<7>          9  blue_out_b<7>        3932   I
 blue_out_b<6>         10  blue_out_b<6>        2841   I
 green_out_b<1>        31  green_out_b<1>      12684   I
 blue_out_b<5>         11  blue_out_b<5>        1718   I
 blue_out_b<4>         12  blue_out_b<4>         694   I
 blue_out_b<3>         13  blue_out_b<3>         625   I
 blue_out_b<2>         14  blue_out_b<2>         449   I
 blue_out_b<1>         15  blue_out_b<1>         161   I
 blue_out_b<0>         16  blue_out_b<0>          67   I
 blue_out_a<7>          1  blue_out_a<7>       12319   O
 blue_out_a<6>          2  blue_out_a<6>       12285   I
 blue_out_a<5>          3  blue_out_a<5>       12111   I
 blue_out_a<4>          4  blue_out_a<4>       11149   I
```

green_out_b<2>	30	green_out_b<2>	12703	I
blue_out_a<3>	5	blue_out_a<3>	10036	I
blue_out_a<2>	6	blue_out_a<2>	9113	I
blue_out_a<1>	7	blue_out_a<1>	8188	I
blue_out_a<0>	8	blue_out_a<0>	7411	I
green_out_b<3>	29	green_out_b<3>	12735	I
green_out_b<4>	28	green_out_b<4>	13006	I
green_out_b<5>	27	green_out_b<5>	13840	I
green_out_b<6>	26	green_out_b<6>	14559	I
pad_XFILT_OUT	82	pad_XFILT_OUT	29	I
green_out_a<7>	17	green_out_a<7>	16176	I
pad_A0	83	pad_A0	6123	I
pad_A1	84	pad_A1	4846	I
pad_Bain	85	pad_Bain	12568	I
pad_Bclampv	86	pad_Bclampv	12489	I
pad_Bmidsc	33	pad_Bmidsc	12625	I
pad_CKEXT	78	pad_CKEXT	397	I
pad_CTL0	34	pad_CTL0	8	I
pad_CTL1	35	pad_CTL1	9	I
pad_CTL2	36	pad_CTL2	10	I
pad_CTL3_MCL	37	pad_CTL3_MCL	11	I
pad_Ckinv	87	pad_Ckinv	9819	I
pad_Coast	88	pad_Coast	552	I
pad_DE	38	pad_DE	16329	I
pad_DVISCL	89	pad_DVISCL	26	I
pad_DVISDA	39	pad_DVISDA	25	I
pad_GClampv	90	pad_GClampv	15665	I
pad_Gain	91	pad_Gain	15700	I
pad_Gmidsc	40	pad_Gmidsc	15706	I
pad_Hsync	92	pad_Hsync	143	I
pad_MDA	41	pad_MDA	24	I
pad_REFOUT	42	pad_REFOUT	16493	I
pad_Rain	93	pad_Rain	16192	I
pad_Rclampv	94	pad_Rclampv	16175	I
pad_Refin	95	pad_Refin	16494	I
pad_Rmidsc	43	pad_Rmidsc	16487	I
pad_Rterm	96	pad_Rterm	15	I
pad_Rx0n	97	pad_Rx0n	21	I
pad_Rx0p	98	pad_Rx0p	20	I
pad_Rx1n	99	pad_Rx1n	19	I
pad_Rx1p	100	pad_Rx1p	18	I
pad_Rx2n	101	pad_Rx2n	17	I
pad_Rx2p	102	pad_Rx2p	16	I
pad_Rxcn	79	pad_Rxcn	23	I
pad_Rxcp	80	pad_Rxcp	22	I
pad_SCANclk	103	pad_SCANclk	12	I
pad_SCANin	104	pad_SCANin	16492	I
pad_SCANout	44	pad_SCANout	7	I
pad_SCL	105	pad_SCL	7231	I
pad_SDA	81	pad_SDA	8072	I
pad_SOGIN	106	pad_SOGIN	15060	I
pad_SOGOUT	45	pad_SOGOUT	16327	I
pad_SyncDT	46	pad_SyncDT	16330	I
pad_VSOUT	47	pad_VSOUT	16328	I
pad_Vsync	107	pad_Vsync	57	I
pad_clamp	108	pad_clamp	8930	I
pad_datack	48	pad_datack	16332	I
pad_datackb	49	pad_datackb	16331	I
pad_hsout	50	pad_hsout	16325	I
red_out_a<0>	58	red_out_a<0>	16317	I

red_out_a<1>	57	red_out_a<1>	16318	I
red_out_a<2>	56	red_out_a<2>	16319	I
red_out_a<3>	55	red_out_a<3>	16320	I
red_out_a<4>	54	red_out_a<4>	16321	I
red_out_a<5>	53	red_out_a<5>	16322	I
red_out_a<6>	52	red_out_a<6>	16323	I
red_out_a<7>	51	red_out_a<7>	16324	I
red_out_b<0>	66	red_out_b<0>	16326	I
red_out_b<1>	65	red_out_b<1>	16310	I
red_out_b<2>	64	red_out_b<2>	16311	I
red_out_b<3>	63	red_out_b<3>	16312	I
red_out_b<4>	62	red_out_b<4>	16313	I
red_out_b<5>	61	red_out_b<5>	16314	I
red_out_b<6>	60	red_out_b<6>	16315	I
red_out_b<7>	59	red_out_b<7>	16316	I
***TOTAL =	108***			

```
.. BIG SCH NODE : AGND                67 CONN =    781
.. BIG SCH NODE : ALGND               68 CONN =    423
.. BIG SCH NODE : ALVDD               69 CONN =    218
.. BIG SCH NODE : AVDD                70 CONN =    718
.. BIG SCH NODE : DGND                71 CONN =    465
.. BIG SCH NODE : DVDD                72 CONN =    353
.. BIG SCH NODE : PVDD                76 CONN =    160
** WARNING ** UN-LABELED BIG SCH NODE =    110 CONN =    235
** WARNING ** UN-LABELED BIG SCH NODE =    133 CONN =    389
** WARNING ** UN-LABELED BIG SCH NODE =    247 CONN =    209
** WARNING ** UN-LABELED BIG SCH NODE =    272 CONN =   2657
** WARNING ** UN-LABELED BIG SCH NODE =    295 CONN =    290
** WARNING ** UN-LABELED BIG SCH NODE =    296 CONN =    298
** WARNING ** UN-LABELED BIG SCH NODE =  28259 CONN =    305
** WARNING ** UN-LABELED BIG SCH NODE =  28260 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28261 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28262 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28264 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28265 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28266 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28279 CONN =    581
.. BIG LAY NODE : AGND                 1 CONN =    781
.. BIG LAY NODE : DVDD                13 CONN =    353
.. BIG LAY NODE : DGND                14 CONN =    465
.. BIG LAY NODE : PVDD                28 CONN =    160
** WARNING ** UN-LABELED BIG LAY NODE =     48 CONN =    209
** WARNING ** UN-LABELED BIG LAY NODE =     53 CONN =    290
.. BIG LAY NODE : AVDD                81 CONN =    718
.. BIG LAY NODE : ALGND               99 CONN =    423
** WARNING ** UN-LABELED BIG LAY NODE =    236 CONN =   2657
** WARNING ** UN-LABELED BIG LAY NODE =    545 CONN =    235
.. BIG LAY NODE : ALVDD              678 CONN =    218
** WARNING ** UN-LABELED BIG LAY NODE =    776 CONN =    389
** WARNING ** UN-LABELED BIG LAY NODE =   1613 CONN =    338
** WARNING ** UN-LABELED BIG LAY NODE =   1615 CONN =    338
** WARNING ** UN-LABELED BIG LAY NODE =   1636 CONN =    338
** WARNING ** UN-LABELED BIG LAY NODE =   4640 CONN =    305
** WARNING ** UN-LABELED BIG LAY NODE =   7387 CONN =    206
** WARNING ** UN-LABELED BIG LAY NODE =   7566 CONN =    298
** WARNING ** UN-LABELED BIG LAY NODE =   7766 CONN =    206
** WARNING ** UN-LABELED BIG LAY NODE =   8037 CONN =    206
** WARNING ** UN-LABELED BIG LAY NODE =   8396 CONN =    581

NUMBER OF VALID CORRESPONDENCE NODE PAIRS =      97
```

```
1       ************************************************
        **********    LVS DEVICE MATCH SUMMARY   **********
        ************************************************


    NUMBER OF UN-MATCHED SCHEMATICS DEVICES      =       0
    NUMBER OF UN-MATCHED LAYOUT     DEVICES      =     349
    NUMBER OF    MATCHED SCHEMATICS DEVICES      =  136392
    NUMBER OF    MATCHED LAYOUT     DEVICES      =  136392
1       ************************************************
        **********  DISCREPANCY POINTS  LISTING  **********
        ************************************************



              NO DISCREPANCIES



        ************************************************
        ********  DEVICE MATCHING SUMMARY BY TYPE  ********
        ************************************************

    TYPE   SUB-TYPE            TOTAL DEVICE     UN-MATCHED DEVICE
                              SCH.      LAY.      SCH.      LAY.

    MOS      P               122678    122679         0         1
    MOS      N               123205    123205         0         0
    BJT      SP                 168       168         0         0
    RES      P                  247       247         0         0
    RES      M1                   1         1         0         0
    RES      N                  403       403         0         0
    RES      M3                   6         6         0         0
    RES      NW                   9         9         0         0
    RES      P1                  12        12         0         0
    RES      P2                   4         4         0         0
    RES      B1                  24        24         0         0
    DIO      ND                  96        96         0         0
    CAP      N                   21        21         0         0
    CAP      PW                  32        32         0         0
    CAP      PS                 170       518         0       348
1       ************************************************
        **********  UN-MATCHED LAYOUT DEVICES    **********
        **********     (LIST UP TO     100 )     **********
        ************************************************


                                  : ?DEV109742 MOS P
                                  :  X=1209.28      Y=-475.68
                                     ?94370, ?77582, ?77583
                                        W = 23.55        L = 5.00
                                  : ?DEV84     CAP PS
                                  :  X=-105.55      Y=-148.10
                                     ?94737, ?94389
```



```
                                  : ?DEV85     CAP PS
                                  :  X=-84.05       Y=-148.10
                                     ?94738, ?94390
                                  : ?DEV86     CAP PS
                                  :  X=-62.55       Y=-148.10
                                     ?94739, ?94391
                                  : ?DEV87     CAP PS
                                  :  X=-41.05       Y=-148.10
                                     ?94740, ?94392
                                  : ?DEV107    CAP PS
                                  :  X=-105.55      Y=-133.40
                                     ?94755, ?94407
                                  : ?DEV110    CAP PS
                                  :  X=-41.05       Y=-133.40
                                     ?94756, ?94408
                                  : ?DEV126    CAP PS
                                  :  X=-105.55      Y=-118.70
                                     ?94767, ?94419
                                  : ?DEV129    CAP PS
                                  :  X=-41.05       Y=-118.70
                                     ?94768, ?94420
                                  : ?DEV145    CAP PS
                                  :  X=-105.55      Y=-104.00
                                     ?94779, ?94431
                                  : ?DEV148    CAP PS
                                  :  X=-41.05       Y=-104.00
                                     ?94780, ?94432
                                  : ?DEV164    CAP PS
                                  :  X=-105.55      Y=-89.30
                                     ?94791, ?94443
                                  : ?DEV167    CAP PS
                                  :  X=-41.05       Y=-89.25
                                     ?94792, ?94444
                                  : ?DEV195    CAP PS
                                  :  X=-105.55      Y=-74.60
                                     ?94804, ?94456
                                  : ?DEV196    CAP PS
                                  :  X=-84.05       Y=-74.60
                                     ?94805, ?94457
                                  : ?DEV197    CAP PS
                                  :  X=-62.55       Y=-74.60
                                     ?94806, ?94458
                                  : ?DEV198    CAP PS
                                  :  X=-41.05       Y=-74.60
                                     ?94807, ?94459
                                  : ?DEV278    CAP PS
                                  :  X=-105.60      Y=614.65
                                     ?94853, ?94505
                                  : ?DEV279    CAP PS
                                  :  X=-84.10       Y=614.65
                                     ?94854, ?94506
                                  : ?DEV280    CAP PS
                                  :  X=-62.60       Y=614.65
                                     ?94855, ?94507
                                  : ?DEV281    CAP PS
                                  :  X=-41.10       Y=614.65
                                     ?94856, ?94508
                                  : ?DEV301    CAP PS
                                  :  X=-105.60      Y=629.35
                                     ?94871, ?94523
```

```
: ?DEV304    CAP PS
:  X=-41.10      Y=629.35
   ?94872, ?94524
: ?DEV320    CAP PS
:  X=-105.60     Y=644.05
   ?94883, ?94535
: ?DEV323    CAP PS
:  X=-41.10      Y=644.05
   ?94884, ?94536
: ?DEV339    CAP PS
:  X=-105.60     Y=658.75
   ?94895, ?94547
: ?DEV342    CAP PS
:  X=-41.10      Y=658.75
   ?94896, ?94548
: ?DEV358    CAP PS
:  X=-105.60     Y=673.45
   ?94907, ?94559
: ?DEV361    CAP PS
:  X=-41.10      Y=673.50
   ?94908, ?94560
: ?DEV389    CAP PS
:  X=-105.60     Y=688.15
   ?94920, ?94572
: ?DEV390    CAP PS
:  X=-84.10      Y=688.15
   ?94921, ?94573
: ?DEV391    CAP PS
:  X=-62.60      Y=688.15
   ?94922, ?94574
: ?DEV392    CAP PS
:  X=-41.10      Y=688.15
   ?94923, ?94575
: ?DEV453    CAP PS
:  X=-103.33     Y=1382.80
   ?94969, ?94621
: ?DEV454    CAP PS
:  X=-81.83      Y=1382.80
   ?94970, ?94622
: ?DEV455    CAP PS
:  X=-60.33      Y=1382.80
   ?94971, ?94623
: ?DEV456    CAP PS
:  X=-38.83      Y=1382.80
   ?94972, ?94624
: ?DEV476    CAP PS
:  X=-103.33     Y=1397.50
   ?94987, ?94639
: ?DEV479    CAP PS
:  X=-38.83      Y=1397.50
   ?94988, ?94640
: ?DEV495    CAP PS
:  X=-103.33     Y=1412.20
   ?94999, ?94651
: ?DEV498    CAP PS
:  X=-38.83      Y=1412.20
   ?95000, ?94652
: ?DEV514    CAP PS
:  X=-103.33     Y=1426.90
   ?95011, ?94663
```



```
: ?DEV517    CAP PS
:  X=-38.83      Y=1426.90
   ?95012, ?94664
: ?DEV533    CAP PS
:  X=-103.33     Y=1441.60
   ?95023, ?94675
: ?DEV536    CAP PS
:  X=-38.83      Y=1441.65
   ?95024, ?94676
: ?DEV564    CAP PS
:  X=-103.33     Y=1456.30
   ?95036, ?94688
: ?DEV565    CAP PS
:  X=-81.83      Y=1456.30
   ?95037, ?94689
: ?DEV566    CAP PS
:  X=-60.33      Y=1456.30
   ?95038, ?94690
: ?DEV567    CAP PS
:  X=-38.83      Y=1456.30
   ?95039, ?94691
: ?DEV62     CAP PS
:  X=-427.75     Y=-162.80
   ?94719, ?94371
: ?DEV63     CAP PS
:  X=-406.35     Y=-162.80
   ?94720, ?94372
: ?DEV64     CAP PS
:  X=-384.95     Y=-162.80
   ?94721, ?94373
: ?DEV81     CAP PS
:  X=-427.75     Y=-148.10
   ?94734, ?94386
: ?DEV82     CAP PS
:  X=-406.35     Y=-148.10
   ?94735, ?94387
: ?DEV83     CAP PS
:  X=-384.95     Y=-148.10
   ?94736, ?94388
: ?DEV104    CAP PS
:  X=-427.75     Y=-133.40
   ?94753, ?94405
: ?DEV106    CAP PS
:  X=-384.95     Y=-133.40
   ?94754, ?94406
: ?DEV123    CAP PS
:  X=-427.75     Y=-118.70
   ?94765, ?94417
: ?DEV125    CAP PS
:  X=-384.95     Y=-118.70
   ?94766, ?94418
: ?DEV142    CAP PS
:  X=-427.75     Y=-104.00
   ?94777, ?94429
: ?DEV144    CAP PS
:  X=-384.95     Y=-104.00
   ?94778, ?94430
: ?DEV161    CAP PS
:  X=-427.75     Y=-89.30
   ?94789, ?94441
```

```
: ?DEV163    CAP PS
:  X=-384.95     Y=-89.30
   ?94790, ?94442
: ?DEV192    CAP PS
:  X=-427.75     Y=-74.60
   ?94801, ?94453
: ?DEV193    CAP PS
:  X=-406.35     Y=-74.60
   ?94802, ?94454
: ?DEV194    CAP PS
:  X=-384.95     Y=-74.60
   ?94803, ?94455
: ?DEV231    CAP PS
:  X=-427.75     Y=-59.90
   ?94820, ?94472
: ?DEV232    CAP PS
:  X=-406.35     Y=-59.90
   ?94821, ?94473
: ?DEV233    CAP PS
:  X=-384.95     Y=-59.90
   ?94822, ?94474
: ?DEV256    CAP PS
:  X=-427.80     Y=599.95
   ?94835, ?94487
: ?DEV257    CAP PS
:  X=-406.40     Y=599.95
   ?94836, ?94488
: ?DEV258    CAP PS
:  X=-385.00     Y=599.95
   ?94837, ?94489
: ?DEV275    CAP PS
:  X=-427.80     Y=614.65
   ?94850, ?94502
: ?DEV276    CAP PS
:  X=-406.40     Y=614.65
   ?94851, ?94503
: ?DEV277    CAP PS
:  X=-385.00     Y=614.65
   ?94852, ?94504
: ?DEV298    CAP PS
:  X=-427.80     Y=629.35
   ?94869, ?94521
: ?DEV300    CAP PS
:  X=-385.00     Y=629.35
   ?94870, ?94522
: ?DEV317    CAP PS
:  X=-427.80     Y=644.05
   ?94881, ?94533
: ?DEV319    CAP PS
:  X=-385.00     Y=644.05
   ?94882, ?94534
: ?DEV336    CAP PS
:  X=-427.80     Y=658.75
   ?94893, ?94545
: ?DEV338    CAP PS
:  X=-385.00     Y=658.75
   ?94894, ?94546
: ?DEV355    CAP PS
:  X=-427.80     Y=673.45
   ?94905, ?94557
```



```
: ?DEV357    CAP PS
:  X=-385.00     Y=673.45
   ?94906, ?94558
: ?DEV386    CAP PS
:  X=-427.80     Y=688.15
   ?94917, ?94569
: ?DEV387    CAP PS
:  X=-406.40     Y=688.15
   ?94918, ?94570
: ?DEV388    CAP PS
:  X=-385.00     Y=688.15
   ?94919, ?94571
: ?DEV405    CAP PS
:  X=-427.80     Y=702.85
   ?94936, ?94588
: ?DEV406    CAP PS
:  X=-406.40     Y=702.85
   ?94937, ?94589
: ?DEV407    CAP PS
:  X=-385.00     Y=702.85
   ?94938, ?94590
: ?DEV431    CAP PS
:  X=-425.53     Y=1368.10
   ?94951, ?94603
: ?DEV432    CAP PS
:  X=-404.13     Y=1368.10
   ?94952, ?94604
: ?DEV433    CAP PS
:  X=-382.73     Y=1368.10
   ?94953, ?94605
: ?DEV450    CAP PS
:  X=-425.53     Y=1382.80
   ?94966, ?94618
: ?DEV451    CAP PS
:  X=-404.13     Y=1382.80
   ?94967, ?94619
: ?DEV452    CAP PS
:  X=-382.73     Y=1382.80
   ?94968, ?94620
: ?DEV473    CAP PS
:  X=-425.53     Y=1397.50
   ?94985, ?94637
: ?DEV475    CAP PS
:  X=-382.73     Y=1397.50
   ?94986, ?94638
: ?DEV492    CAP PS
:  X=-425.53     Y=1412.20
   ?94997, ?94649
: ?DEV494    CAP PS
:  X=-382.73     Y=1412.20
   ?94998, ?94650
: ?DEV511    CAP PS
:  X=-425.53     Y=1426.90
   ?95009, ?94661

THE REST OF UN-MATCHED LAYOUT DEVICES ARE NOT LISTED

1  ************************************************
```

```
**********     LVS SUMMARY (REPEATED)    **********
*************************************************

*************************************************
**********    LVS DEVICE MATCH SUMMARY   **********
*************************************************


NUMBER OF UN-MATCHED SCHEMATICS DEVICES      =       0
NUMBER OF UN-MATCHED LAYOUT     DEVICES      =     349
NUMBER OF    MATCHED SCHEMATICS DEVICES      =  136392
NUMBER OF    MATCHED LAYOUT     DEVICES      =  136392

*************************************************
********  DEVICE MATCHING SUMMARY BY TYPE  ********
*************************************************
```

TYPE	SUB-TYPE	TOTAL DEVICE SCH.	TOTAL DEVICE LAY.	UN-MATCHED DEVICE SCH.	UN-MATCHED DEVICE LAY.
MOS	P	122678	122679	0	1
MOS	N	123205	123205	0	0
BJT	SP	168	168	0	0
RES	P	247	247	0	0
RES	M1	1	1	0	0
RES	N	403	403	0	0
RES	M3	6	6	0	0
RES	NW	9	9	0	0
RES	P1	12	12	0	0
RES	P2	4	4	0	0
RES	B1	24	24	0	0
DIO	ND	96	96	0	0
CAP	N	21	21	0	0
CAP	PW	32	32	0	0
CAP	PS	170	518	0	348

```
*************************************************
**/W* -- SCHEMATIC AND LAYOUT MAY NOT MATCH      **
**         CHECK ALL ABOVE DISCREPANCY           **
**         AND WARNING MESSAGES                  **
*************************************************
```

```
*/N*    DRACULA  ( REV. 4.7.03-2000 /   SUN-4 S5R4   /GENDATE: 29-FEB/2000  )
                  *** ( Copyright 1995, Cadence ) ***
*/N*     EXEC TIME =12:00:14       DATE =14-MAY-2002     HOSTNAME =      xkr
```

---------- ALL ERROR CELLS LISTING ----------

PPSFT35	NPSFT35	NWSFT35	PWRSC62	GNDSC62
NWELW61	NWELS61	NWELH61	NWELN61	BNMARK61
NDW61	NMOSW61	NDSP61	NWNPE61	NWNPS61
HWELDS61	HWELD61	NWINP61	LVTW61	LVTS61
LVNWS61	LVNPS61	HWELLV61	LVTSN61	MVTW61
MVTS61	MVNWS61	MVNWE61	NPER61	MVTER61
MVNPS61	LVMVS61	HWELMV61	MVTSN61	PDWB61
PMOSW61	PDSP61	PPNPSP61	NPPPSP61	NWPPO61
NWPPS61	PPMVSP61	PPLVS61	PPMVS61	MVTERA61
ESDNW561	ESDNS561	NPOUE561	PPOUE561	NPINE561
ES5ANP61	ES5VER61	ESDNW361	ESDNS361	ESNPE361
COES3E61	ESPOS361	ES3VER61	E3VER161	E3VER261
E3VER361	ESDER61	TPOLW61	POLYS61	ESPOW61
PNFPS61	POENDC61	POEND61	PONDE61	POPDE61
P1INW161	DITOP161	P1OUW161	PRINW161	PROUW161
NWNGT61	P1PCS61	CAPER61	BMARK61	PCAPER61
POMVE61	POLVE61	MVPOW61	NVPOW61	N5VML61
P5VML61	CONTW61	BADCT61	CONTS61	CONFP61
CONWP61	CONPE61	COPPE61	COMVE61	COLVE61
CONTPS61	POGASP61	PCDISP61	IOCIND61	BIOCT61
IOCGAS61	IOCGBS61	COINWI61	MET1W61	MET1S61
CONNTA61	CONNS61	MET1IO61	WMET1S61	VIAW61
BADVI61	VIASP61	M1VIA61	MET2W61	MET2S61
M2VIA61	MET2IO61	WMET2S61	VIA2W61	BADVI261
VIA2SP61	M1VIA261	MET3W61	MET3S61	M3VIA261
MET3IO61	WMET3S61	P2RSW61	P2RSS61	P2ACS61
P1P2S61	COP2E61	P2CPW61	P2CPS61	P2INP161
COINP261	COP2S61	P2RINW61	P2ROUW61	MT1PAD61
MT2PAD61	MT3PAD61	PV1M1E61	PV1M2E61	PADV1S61
PV2M2E61	PV2M3E61	PADV2S61	PV1PV261	ROMNPW61
ROMNPS61	NPRMNP61	RMNPPP61	FUSEW61	FSWGT61
BDFUS61	BADFUS61	FUPOW61	FUPOA61	FUPOAA61
COFUE61	FCTER61	ND5W61	ND5SP61	ND5NDS61
ND5LVS61	ND5MVS61	ND5PDS61	PD5W61	PD5SP61
PD5NWE61	PD5NWS61	PD5NDS61	PD5PDS61	PGTER61
PD5MVS61	GANPE61	NPPDS61	NSDIW61	NSDIS61
NSGAO61	NPIPO61	NPPMS61	PSDIW61	PSDIS61
NPESS61	GAPPE61	PPNDS61	PSGAO61	PPIPO61
PPNMS61	ERR161	PPESS61	GAPNE61	PPANP61
ODOD2E61	OD2SP61	ODOD2S61	NSD3VW61	NSD3VS61
N3ODS61	GA3VNE61	N3IPO61	N3PMS61	ERR261
NSD5VW61	NSD5VS61	N5ODS61	O2N5E61	N5IPO61
N5PMS61	ERR361	PWELW61	PWELS61	VTIIW61
VTIIS61	PFLDW61	PFLDS61	FPTPW61	FPPADW61
PADSS61	M1FPSP61	M2FPSP61	M3FPSP61	

BEGIN AT TIME =12:00:14 DATE =14-MAY-2002

INDISK PRIMARY CELL : 9887A

CELL	NWSFT35	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PWRSC62	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	GNDSC62	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELH61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELN61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	BNMARK61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NDW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NMOSW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NDSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWNPE61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWNPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELDS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELD61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWINP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVTW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVTS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVNWS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVNPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELLV61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVTSN61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVNWS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVNWE61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPER61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTER61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVNPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVMVS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELMV61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTSN61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PDWB61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PMOSW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PDSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPNPSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPPPSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWPPO61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWPPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPMVSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPLVS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPMVS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTERA61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNW561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNS561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPOUE561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPOUE561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPINE561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ES5ANP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ES5VER61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNW361	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNS361	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESNPE361	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	COES3E61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESPOS361	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ES3VER61	DELETED	BECAUSE OF NO OUTPUT DATA

```
CELL E3VER161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL E3VER261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL E3VER361   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ESDER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL TPOLW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POLYS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ESPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PNFPS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POENDC61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL POEND61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PONDE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POPDE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1INW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL DITOP161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1OUW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PRINW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PROUW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NWNGT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1PCS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CAPER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BMARK61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PCAPER61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL POMVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POLVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MVPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NVPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5VML61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P5VML61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONTW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADCT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONTS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONFP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONWP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COPPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COMVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COLVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONTPS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL POGASP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PCDISP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL IOCIND61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL BIOCT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL IOCGAS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ICCGBS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COINW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET1W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET1S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONMTA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONNS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET1IO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL WMET1S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIAW61     DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADVI61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIASP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL M1VIA61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET2W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET2S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL M2VIA61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET2IO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL WMET2S61   DELETED  BECAUSE OF NO OUTPUT DATA
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CELL VIA2W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADVI261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIA2SP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL M1VIA261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET3W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET3S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL M3VIA261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET3IO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL WMET3S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2RSW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2RSS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2ACS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1P2S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COP2E61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2CPW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2CPS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2INP161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COINP261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COP2S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2RINW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2ROUW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MT1PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MT2PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MT3PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV1M1E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV1M2E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADV1S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV2M2E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV2M3E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADV2S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV1PV261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ROMNPW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ROMNPS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPRMNP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL RMNPPP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUSEW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FSWGT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BDFUS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADFUS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUPOA61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUPOAA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COFUE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FCTER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5W61     DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5SP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5NDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5LVS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5MVS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5PDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5W61     DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5SP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5NWE61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5NWS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5NDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5PDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PGTER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5MVS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL GANPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPPDS61    DELETED  BECAUSE OF NO OUTPUT DATA
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CELL NSDIW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSDIS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSGAO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPIPO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPPMS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PSDIW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PSDIS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPESS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL GAPPE61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPNDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PSGAO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPIPO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPNMS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ERR161    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPESS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL GAPNE61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPANP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ODOD2E61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL OD2SP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ODOD2S61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD3VW61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD3VS61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL N3ODS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL GA3VNE61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL N3IPO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL N3PMS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ERR261    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD5VW61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD5VS61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5ODS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL O2N5E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5IPO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5PMS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ERR361    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PWELW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PWELS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VTIIW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VTIIS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PFLDW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PFLDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL FPTPW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL FPPADW61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADSS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL M1FPSP61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL M2FPSP61  DELETED  BECAUSE OF NO OUTPUT DATA
CELL M3FPSP61  DELETED  BECAUSE OF NO OUTPUT DATA
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---------- OUTPUT CELL SUMMARY ----------

CELL-NAME	LAYER # DATATYPE	WINDOW				# OF POLYGONS (LINE SEGMENTS)	TEXTS
PPSFT35	35/ 0	-1754.30	-2029.80	1830.70	1513.65	1206	0
NPSFT35	35/ 0	821.30	455.08	1260.60	758.08	8	0


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 OUTDISK PRIMARY CELL : OUT9887A
       WINDOW :      -1754.30    -2029.80     1830.70     1513.65

              ENDED    AT  TIME =12:00:14       DATE =14-MAY-2002

1          ******    PROBLEM GEOMETRY ERROR LISTING   ******

 CELL: ESD_A              LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      95.250      222.000      93.750      223.500      81.050      223.500
      81.050      198.500      79.550      197.000      76.650      197.000
      75.150      198.500      75.150      206.750      76.900      208.500
      79.550      208.500      81.050      207.000      81.050      223.500
      74.550      223.500      74.550      171.900      73.050      170.400
      70.150      170.400      68.650      171.900      68.650      180.400
      70.150      181.900      73.050      181.900      74.550      180.400
      74.550      223.500      70.150      223.500      68.650      225.000
      68.650      254.600      54.400      254.600      52.450      252.650
      52.450      163.000      95.250      163.000      95.250      222.000

 CELL: AESD_SMALL_SR_A    LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      97.800      212.350      96.600      213.550      83.900      213.550
      83.900      188.550      82.400      187.050      79.500      187.050
      79.400      187.150      79.400      171.700      77.400      171.700
      77.400      147.450      75.900      145.950      73.000      145.950
      71.500      147.450      71.500      155.950      73.000      157.450
      75.900      157.450      77.400      155.950      77.400      173.700
      79.400      173.700      79.400      187.150      78.000      188.550
      78.000      196.800      79.750      198.550      82.400      198.550
      83.900      197.050      83.900      213.550      73.000      213.550
      71.500      215.050      71.500      244.650      57.225      244.650
      54.800      242.225      54.800      130.000      79.800      130.000
      79.800      140.550      81.800      140.550      81.800      130.000
      97.800      130.000      97.800      212.350

 CELL: AESD_SMALL_SR      LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      97.800      212.350      96.600      213.550      83.900      213.550
      83.900      188.550      82.400      187.050      79.500      187.050
      79.400      187.150      79.400      171.700      77.400      171.700
      77.400      147.450      75.900      145.950      73.000      145.950
      71.500      147.450      71.500      155.950      73.000      157.450
      75.900      157.450      77.400      155.950      77.400      173.700
      79.400      173.700      79.400      187.150      78.000      188.550
      78.000      196.875      79.675      198.550      82.400      198.550
      83.900      197.050      83.900      213.550      73.000      213.550
      71.500      215.050      71.500      244.650      57.225      244.650
      54.800      242.225      54.800      130.000      79.800      130.000
      79.800      140.550      81.800      140.550      81.800      130.000
      97.800      130.000      97.800      212.350

 CELL: ESD                LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      95.250      212.000      93.750      213.500      81.050      213.500
      81.050      188.500      79.550      187.000      76.650      187.000
      75.150      188.500      75.150      196.750      76.900      198.500
      79.550      198.500      81.050      197.000      81.050      213.500
      74.550      213.500      74.550      161.900      73.050      160.400
      70.150      160.400      68.650      161.900      68.650      170.400
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      70.150      171.900       73.050      171.900       74.550      170.400
      74.550      213.500       70.150      213.500       68.650      215.000
      68.650      244.600       54.250      244.600       52.450      242.800
      52.450      153.000       95.250      153.000       95.250      212.000

 CELL: AESD_NSR              LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      97.800      212.350       96.600      213.550       83.900      213.550
      83.900      188.550       82.400      187.050       79.500      187.050
      79.400      187.150       79.400      171.700       77.400      171.700
      77.400      147.450       75.900      145.950       73.000      145.950
      71.500      147.450       71.500      155.950       73.000      157.450
      75.900      157.450       77.400      155.950       77.400      173.700
      79.400      173.700       79.400      187.150       78.000      188.550
      78.000      196.850       79.700      198.550       82.400      198.550
      83.900      197.050       83.900      213.550       73.000      213.550
      71.500      215.050       71.500      244.650       57.175      244.650
      54.800      242.275       54.800      130.000       79.800      130.000
      79.800      140.550       81.800      140.550       81.800      130.000
      97.800      130.000       97.800      212.350

 CELL: ESD_POWER_NDIO        LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      34.800       21.600       24.400       21.600       24.400       30.000
      26.400       30.000       26.400       33.800       24.400       33.800
      24.400       21.600       25.000       21.600       23.500       20.100
      23.500        0.900       22.000       -0.600        0.900       -0.600
      -0.600        0.900       -0.600       63.400        0.900       64.900
      22.000       64.900       23.500       63.400       23.500       45.100
      25.000       43.600       34.800       43.600       34.800       21.600

 CELL: ESDOUT                LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
     114.000      230.500      112.500      232.000      109.500      232.000
     109.500      209.000       94.700      209.000       94.700      211.000
     109.500      211.000      109.500      232.000       89.500      232.000
      89.500      207.000       88.000      205.500       84.500      205.500
      83.000      207.000       83.000      215.500       84.500      217.000
      88.000      217.000       89.500      215.500       89.500      232.000
      78.600      232.000       77.100      233.500       77.100      263.400
      60.900      263.400       60.900      130.000       80.000      130.000
      80.000      130.800       82.000      130.800       82.000      130.000
      85.900      130.000       85.900      188.000       89.000      188.000
      89.000      145.050       98.000      145.050       98.000      130.800
     100.000      130.800      100.000      130.000      114.000      130.000
     114.000      230.500


          ***** END OF PROBLEM GEOMETRY LISTING *****
              NUMBER OF ACUTE ANGLE INPUT POLYGONS =          7
1         **********    INPUT COMMANDS LISTING   **********

    1:*DESCRIPTION
    2:PRIMARY = 9887A
    3:INDISK = 9887a.gin
    4:OUTDISK = 9887a.gou
    5:POWER-NODE =  ALVDD AVDD DVDD OVDD PVDD
    6:GROUND-NODE =  AGND ALGND DGND OGND PGND SUBGND
    7:TEXT-PRI-ONLY = YES
    8:PRINTFILE = X887apnchkfp
    9:FLAGNON45 = YES
   10:FLAG-OFFGRID = YES
   11:PATH-W-OFFGRID = YES
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   12:FLAG-PTH-OFFGRID = YES
   13:FLAG-ACUTEANGLE = YES
   14:ACUTE-CLIP = NO
   15:SYSTEM = GDS2
   16:RESOLUTION = .025 MIC
   17:SCALE = .001 MIC
   18:KEEPDATA = YES
   19:FLAG-SELFINTERS = YES
   20:SUMMARY-ONLY = NO
   21:MODE = EXEC NOW
   22:UNSPEC-PARA = EXCEPT-SUBSTRATE
   23:KEEP-SHORT-MOS = Yes
   24:KEEP-SHORT-RES = NO
   25:KEEP-SHORT-CAP = Yes
   26:CNAMES-CSEN = YES
   27:SMASH-CAP-TYPE = NO
   28:DELCEL = NONACT SEAL_RING
   29:MODEL = MOS[N],N MOS[P],P LDD[XL],XL
   30:MODEL = BJT[SP],SP
   31:MODEL = CAP[N],N
   32:MODEL = CAP[PS],PS CAP[PW],PW
   33:MODEL = RES[M1],M1 RES[M2],M2 RES[P2],P2 RES[B2],B2
   34:MODEL = RES[P],P RES[N],N RES[P1],P1 RES[B1],B1
   35:MODEL = DIO[ND],ND
   36:MODEL = DIO[PP],PP
   37:MODEL = MOS[NN],NN MOS[NM],NM
   38:MODEL = RES[NW],NW
   39:MODEL = RES[FU],FU
   40:UNIT = AREA,P PERIMETER,U
   41:PARSET LCAP AREA PERI C OVPR P TPR CLL
   42:PARSET ANTE AREA
   43:DIODESEQ = A1 P1 A2 P2
   44:UNSPEC-PARA      = EXCEPT-SUBSTRATE
   45:OUTPUT-ONE-LAYER = NO
   46:*END
   47:*INPUT-LAYER
   48:TNWELL    = 1
   49:TNP       = 2
   50:ESDN3V    = 3
   51:TPP       = 5
   52:TPOLY     = 4
   53:ROMNP     = 37
   54:TCONT     = 7
   55:TMET1     = 8       TEXT = 21 ATTACH MET1

   56:VIA       = 9
   57:TME2T     = 10      TEXT = 22 ATTACH ME2T

   58:PAD       = 11
   59:METMARK   = 13
   60:VI2       = 14
   61:TME3T     = 15      TEXT = 24 ATTACH ME3T

   62:LOWVT     = 6
   63:MEDVT     = 34
   64:VTII      = 41
   65:PWEL      = 40
   66:ESDN5V    = 30
   67:PWSUB     = 33
   68:NLDD3V    = 38
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*******************************************************************************
*/N*    DRACULA  ( REV. 4.7.03-2000 /   SUN-4 S5R4   /GENDATE: 29-FEB/2000  )
                  *** ( Copyright 1995, Cadence ) ***
*/N*     EXEC TIME =11:46:40       DATE =14-MAY-2002     HOSTNAME =      xkr
*******************************************************************************
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---------- ALL ERROR CELLS LISTING ----------

PPSFT35	NPSFT35	NWSFT35	PWRSC62	GNDSC62
NWELW61	NWELS61	NWELH61	NWELN61	BNMARK61
NDW61	NMOSW61	NDSP61	NWNPE61	NWNPS61
HWELDS61	HWELD61	NWINP61	LVTW61	LVTS61
LVNWS61	LVNPS61	HWELLV61	LVTSN61	MVTW61
MVTS61	MVNWS61	MVNWE61	NPER61	MVTER61
MVNPS61	LVMVS61	HWELMV61	MVTSN61	PDWB61
PMOSW61	PDSP61	PPNPSP61	NPPPSP61	NWPPO61
NWPPS61	PPMVSP61	PPLVS61	PPMVS61	MVTERA61
ESDNW561	ESDNS561	NPOUE561	PPOUE561	NPINE561
ES5ANP61	ES5VER61	ESDNW361	ESDNS361	ESNPE361
COES3E61	ESPOS361	ES3VER61	E3VER161	E3VER261
E3VER361	ESDER61	TPOLW61	POLYS61	ESPOW61
PNFPS61	POENDC61	POEND61	PONDE61	POPDE61
P1INW161	DITOP161	P1OUW161	PRINW161	PROUW161
NWNGT61	P1PCS61	CAPER61	BMARK61	PCAPER61
POMVE61	POLVE61	MVPOW61	NVPOW61	N5VML61
P5VML61	CONTW61	BADCT61	CONTS61	CONPP61
CONWP61	CONPE61	COPPE61	COMVE61	COLVE61
CONTPS61	POGASP61	PCDISP61	IOCIND61	BIOCT61
IOCGAS61	IOCGBS61	COINWI61	MET1W61	MET1S61
CONWTA61	CONNS61	MET1IO61	WMET1S61	VIAW61
BADVI61	VIASP61	M1VIA61	MET2W61	MET2S61
M2VIA61	MET2IO61	WMET2S61	VIA2W61	BADVI261
VIA2SP61	M1VIA261	MET3W61	MET3S61	M3VIA261
MET3IO61	WMET3S61	P2RSW61	P2RSS61	P2ACS61
P1P2S61	COP2E61	P2CPW61	P2CPS61	P2INP161
COINP261	COP2S61	P2RINW61	P2ROUW61	MT1PAD61
MT2PAD61	MT3PAD61	PV1M1E61	PV1M2E61	PADV1S61
PV2M2E61	PV2M3E61	PADV2S61	PV1PV261	ROMNPW61
ROMNPS61	NPRMNP61	RMNPPP61	FUSEW61	FSWGT61
BDFUS61	BADFUS61	FUPOW61	FUPOA61	FUPOAA61
COFUE61	FCTER61	ND5W61	ND5SP61	ND5NDS61
ND5LVS61	ND5MVS61	ND5PDS61	PD5W61	PD5SP61
PD5NWE61	PD5NWS61	PD5NDS61	PD5PDS61	PGTER61
PD5MVS61	GANPE61	NPPDS61	NSDIW61	NSDIS61
NSGAO61	NPIPO61	NPPMS61	PSDIW61	PSDIS61
NPESS61	GAPPE61	PPNDS61	PSGAO61	PPIPO61
PPNMS61	ERR161	PPESS61	GAPNE61	PPANP61
ODOD2E61	OD2SP61	ODOD2S61	NSD3VW61	NSD3VS61
N3ODS61	GA3VNE61	N3IPO61	N3PMS61	ERR261
NSD5VW61	NSD5VS61	N5ODS61	O2N5E61	N5IPO61
N5PMS61	ERR361	PWELW61	PWELS61	VTIIW61
VTIIS61	PFLDW61	PFLDS61	SLITW61	NOTCW61
NOTC3W61	ER361	ER161	ER261	ER461
ER561	ER661	ER761	ER861	ER961
F1AREA61	F1ERR161	F1WIDT61	F2AREA61	F2ERR161
F2WIDT61	F3AREA61	F3ERR161	F3WIDT61	BD2PAD61
PADEM161	PADEM261	PADEM361	PADEM461	PADEM561

PADEM661

BEGIN AT TIME =11:46:40 DATE =14-MAY-2002

INDISK PRIMARY CELL : 9887A

CELL	NWSFT35	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PWRSC62	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	GNDSC62	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELH61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWELN61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	BNMARK61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NDW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NMOSW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NDSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWNPE61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWNPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELDS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELD61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWINP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVTW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVTS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVNWS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVNPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELLV61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVTSN61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVNWS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVNWE61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPER61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTER61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVNPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	LVMVS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	HWELMV61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTSN61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PDWB61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PMOSW61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PDSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPNPSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPPPSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWPPO61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NWPPS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPMVSP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPLVS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPMVS61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	MVTERA61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNW561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNS561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPOUE561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	PPOUE561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	NPINE561	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ES5ANP61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ES5VER61	DELETED	BECAUSE OF NO OUTPUT DATA
CELL	ESDNW361	DELETED	BECAUSE OF NO OUTPUT DATA

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CELL ESNPE361   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COES3E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ESPOS361   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ES3VER61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL E3VER161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL E3VER261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL E3VER361   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ESDER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL TPOLW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POLYS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ESPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PNFPS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POENDC61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL POEND61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PONDE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POPDE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1INW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL DITOP161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1OUW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PRINW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PROUW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NWNGT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1PCS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CAPER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BMARK61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PCAPER61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL POMVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL POLVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MVPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NVPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5VML61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P5VML61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONTW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADCT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONTS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONFP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONWP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COPPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COMVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COLVE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONTPS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL POGASP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PCDISP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL IOCIND61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL BIOCT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL IOCGAS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL IOCGBS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COINW161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET1W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET1S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONMTA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL CONNS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET1IO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL WMET1S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIAW61     DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADVI61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIASP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL M1VIA61    DELETED  BECAUSE OF NO OUTPUT DATA
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CELL MET2W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET2S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL M2VIA61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET2IO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL WMET2S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIA2W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADVI261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL VIA2SP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL M1VIA261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET3W61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET3S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL M3VIA261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MET3IO61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL WMET3S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2RSW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2RSS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2ACS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P1P2S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL COP2E61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2CPW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2CPS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2INP161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COINP261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COP2S61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2RINW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL P2ROUW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MT1PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MT2PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL MT3PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV1M1E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV1M2E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADV1S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV2M2E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV2M3E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADV2S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PV1PV261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ROMNPW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ROMNPS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPRMNP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL RMNPPP61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUSEW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FSWGT61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BDFUS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL BADFUS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUPOW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUPOA61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FUPOAA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL COFUE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL FCTER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5W61     DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5SP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5NDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5LVS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5MVS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ND5PDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5W61     DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5SP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5NWE61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5NWS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5NDS61   DELETED  BECAUSE OF NO OUTPUT DATA
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CELL PD5PDS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PGTER61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PD5MVS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL GANPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPPDS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSDIW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSDIS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSGAO61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPIPO61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPPMS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PSDIW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PSDIS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NPESS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL GAPPE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPNDS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PSGAO61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPIPO61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPNMS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ERR161     DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPESS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL GAPNE61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PPANP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ODOD2E61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL OD2SP61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ODOD2S61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD3VW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD3VS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL N3ODS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL GA3VNE61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL N3IPO61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL N3PMS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ERR261     DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD5VW61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL NSD5VS61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5ODS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL O2N5E61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5IPO61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL N5PMS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL ERR361     DELETED  BECAUSE OF NO OUTPUT DATA
CELL PWELW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PWELS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL VTIIW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL VTIIS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PFLDW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL PFLDS61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL SLITW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NOTCW61    DELETED  BECAUSE OF NO OUTPUT DATA
CELL NOTC3W61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER361      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER161      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER261      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER461      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER561      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER661      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER761      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER861      DELETED  BECAUSE OF NO OUTPUT DATA
CELL ER961      DELETED  BECAUSE OF NO OUTPUT DATA
CELL F1AREA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F1ERR161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F1WIDT61   DELETED  BECAUSE OF NO OUTPUT DATA
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CELL F2AREA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F2ERR161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F2WIDT61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F3AREA61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F3ERR161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL F3WIDT61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL BD2PAD61   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADEM161   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADEM261   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADEM361   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADEM461   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADEM561   DELETED  BECAUSE OF NO OUTPUT DATA
CELL PADEM661   DELETED  BECAUSE OF NO OUTPUT DATA
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---------- OUTPUT CELL SUMMARY ----------

CELL-NAME	LAYER # DATATYPE	---------- W I N D O W ----------				# OF POLYGONS (LINE SEGMENTS)	TEXT
PPSFT35	35/ 0	-1754.30	-2029.80	1830.70	1513.65	1206	0
NPSFT35	35/ 0	821.30	455.08	1260.60	758.08	8	0

OUTDISK PRIMARY CELL : OUT9887A
WINDOW : -1754.30 -2029.80 1830.70 1513.65

ENDED AT TIME =11:46:42 DATE =14-MAY-2002

1 ****** PROBLEM GEOMETRY ERROR LISTING ******

CELL: ESD_A		LAYER: 8	PRO/GEO:ACUTE		- PROCESSED
95.250	222.000	93.750	223.500	81.050	223.500
81.050	198.500	79.550	197.000	76.650	197.000
75.150	198.500	75.150	206.750	76.900	208.500
79.550	208.500	81.050	207.000	81.050	223.500
74.550	223.500	74.550	171.900	73.050	170.400
70.150	170.400	68.650	171.900	68.650	180.400
70.150	181.900	73.050	181.900	74.550	180.400
74.550	223.500	70.150	223.500	68.650	225.000
68.650	254.600	54.400	254.600	52.450	252.650
52.450	163.000	95.250	163.000	95.250	222.000

CELL: AESD_SMALL_SR_A		LAYER: 8	PRO/GEO:ACUTE		- PROCESSED
97.800	212.350	96.600	213.550	83.900	213.550
83.900	188.550	82.400	187.050	79.500	187.050
79.400	187.150	79.400	171.700	77.400	171.700
77.400	147.450	75.900	145.950	73.000	145.950
71.500	147.450	71.500	155.950	73.000	157.450
75.900	157.450	77.400	155.950	77.400	173.700
79.400	173.700	79.400	187.150	78.000	188.550
78.000	196.800	79.750	198.550	82.400	198.550
83.900	197.050	83.900	213.550	73.000	213.550

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      71.500      215.050       71.500      244.650       57.225      244.650
      54.800      242.225       54.800      130.000       79.800      130.000
      79.800      140.550       81.800      140.550       81.800      130.000
      97.800      130.000       97.800      212.350

CELL: AESD_SMALL_SR          LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      97.800      212.350       96.600      213.550       83.900      213.550
      83.900      188.550       82.400      187.050       79.500      187.050
      79.400      187.150       79.400      171.700       77.400      171.700
      77.400      147.450       75.900      145.950       73.000      145.950
      71.500      147.450       71.500      155.950       73.000      157.450
      75.900      157.450       77.400      155.950       77.400      173.700
      79.400      173.700       79.400      187.150       78.000      188.550
      78.000      196.875       79.675      198.550       82.400      198.550
      83.900      197.050       83.900      213.550       73.000      213.550
      71.500      215.050       71.500      244.650       57.225      244.650
      54.800      242.225       54.800      130.000       79.800      130.000
      79.800      140.550       81.800      140.550       81.800      130.000
      97.800      130.000       97.800      212.350

CELL: ESD                    LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      95.250      212.000       93.750      213.500       81.050      213.500
      81.050      188.500       79.550      187.000       76.650      187.000
      75.150      188.500       75.150      196.750       76.900      198.500
      79.550      198.500       81.050      197.000       81.050      213.500
      74.550      213.500       74.550      161.900       73.050      160.400
      70.150      160.400       68.650      161.900       68.650      170.400
      70.150      171.900       73.050      171.900       74.550      170.400
      74.550      213.500       70.150      213.500       68.650      215.000
      68.650      244.600       54.250      244.600       52.450      242.800
      52.450      153.000       95.250      153.000       95.250      212.000

CELL: AESD_NSR               LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      97.800      212.350       96.600      213.550       83.900      213.550
      83.900      188.550       82.400      187.050       79.500      187.050
      79.400      187.150       79.400      171.700       77.400      171.700
      77.400      147.450       75.900      145.950       73.000      145.950
      71.500      147.450       71.500      155.950       73.000      157.450
      75.900      157.450       77.400      155.950       77.400      173.700
      79.400      173.700       79.400      187.150       78.000      188.550
      78.000      196.850       79.700      198.550       82.400      198.550
      83.900      197.050       83.900      213.550       73.000      213.550
      71.500      215.050       71.500      244.650       57.175      244.650
      54.800      242.275       54.800      130.000       79.800      130.000
      79.800      140.550       81.800      140.550       81.800      130.000
      97.800      130.000       97.800      212.350

CELL: ESD_POWER_NDIO         LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
      34.800       21.600       24.400       21.600       24.400       30.000
      26.400       30.000       26.400       33.800       24.400       33.800
      24.400       21.600       25.000       21.600       23.500       20.100
      23.500        0.900       22.000       -0.600        0.900       -0.600
      -0.600        0.900       -0.600       63.400        0.900       64.900
      22.000       64.900       23.500       63.400       23.500       45.100
      25.000       43.600       34.800       43.600       34.800       21.600

CELL: ESDOUT                 LAYER:  8  PRO/GEO:ACUTE              - PROCESSED
     114.000      230.500      112.500      232.000      109.500      232.000
     109.500      209.000       94.700      209.000       94.700      211.000
     109.500      211.000      109.500      232.000       89.500      232.000
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      89.500      207.000       88.000      205.500       84.500      205.500
      83.000      207.000       83.000      215.500       84.500      217.000
      88.000      217.000       89.500      215.500       89.500      232.000
      78.600      232.000       77.100      233.500       77.100      263.400
      60.900      263.400       60.900      130.000       80.000      130.000
      80.000      130.800       82.000      130.800       82.000      130.000
      85.900      130.000       85.900      188.000       89.000      188.000
      89.000      145.050       98.000      145.050       98.000      130.800
     100.000      130.800      100.000      130.000      114.000      130.000
     114.000      230.500


          ***** END OF PROBLEM GEOMETRY LISTING *****
              NUMBER OF ACUTE ANGLE INPUT POLYGONS =          7
1          **********     INPUT COMMANDS LISTING    **********

    1:*DESCRIPTION
    2:PRIMARY = 9887A
    3:INDISK = 9887a.gin
    4:OUTDISK = 9887a.gcu
    5:POWER-NODE =  ALVDD AVDD DVDD OVDD PVDD
    6:GROUND-NODE =  AGND ALGND DGND OGND PGND SUBGND
    7:TEXT-PRI-ONLY = YES
    8:PRINTFILE = X887aadrc
    9:FLAGNON45 = YES

 1157:*END
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*******************************************************************************
*/N*    DRACULA  ( REV. 4.7.03-2000 /   SUN-4 S5R4   /GENDATE: 29-FEB/2000  )
                  *** ( Copyright 1995, Cadence ) ***
*/N*     EXEC TIME =10:46:51        DATE =14-MAY-2002      HOSTNAME = firebird
*******************************************************************************

 INDISK PRIMARY CELL : 9887A


 ********** LVSNET SUMMARY REPORT **********


  WEFFECT VALUE= 0.0000000


 ******* REDUCE (LAYOUT) SUMMARY REPORT *******


 ******* STATISTICS BEFORE REDUCE ****

       MOS      BJT      RES    DIODE      CAP      UND      BOX     CELL      LDD
    291615      195      961      163      663        0        0        0        0

  OPTION TO SMASH PARALLEL DEVICES IS -- ON
  OPTION TO CONSTRUCT MOS PARALLEL/SERIES STRUCTURES IS --  ON
  OPTION TO SMASH PSEUDO PARALLEL DEVICES IS --  ON
  OPTION TO FORM CMOS GATES IS --  ON
  OPTION TO EXTRACT SUBSTRATE NODES OF GATES IS -- OFF


 ******* STATISTICS AFTER REDUCE ****
       MOS      BJT      RES      INV    DIODE      CAP     SDWI     PDWI     SUPI
     58627      168      706    56627       96      571       63        0       58
      PUPI     SDW      PDW      SUP      PUP      AND       OR      AOI     NAND
       183      165      128      387      131      513     2521      384    11199
       NOR      OAI      UND      BOX     CELL      LDD     SMID     PMID   MOSCAP
      1929     1925        0        0        0        0        0        0      181
      DRAM     SRAM
         0        0

 ******* REDUCE (SCHEMATIC) SUMMARY REPORT *******


 ******* STATISTICS BEFORE REDUCE ****

       MOS      BJT      RES    DIODE      CAP      UND      BOX     CELL      LDD
    268800      191      807      115      264        0        0        0        0


 ******* STATISTICS AFTER REDUCE ****
       MOS      BJT      RES      INV    DIODE      CAP     SDWI     PDWI     SUPI
     58626      168      706    56627       96      223       63        0       58
      PUPI     SDW      PDW      SUP      PUP      AND       OR      AOI     NAND
       183      165      128      387      131      513     2521      384    11199
       NOR      OAI      UND      BOX     CELL      LDD     SMID     PMID   MOSCAP
      1929     1925        0        0        0        0        0        0      181
      DRAM     SRAM
         0        0
```



```
 ********** LVS REPORT **********

       DATE : 14-MAY-2002
       TIME : 11:29:32


       PRINTLINE      =     1000
       WPERCENT(MOS)  =    2.000 %
       LPERCENT(MOS)  =    1.000 %
       BJT EMITTER AREA CHECK: EMAPER=    5.000 %
       CAPACITOR VALUE CHECK:  CVPER=    5.000 %
       RESISTOR WIDTH CHECK:   RESWPR=   2.000 %
       RESISTOR LENGTH CHECK:  RESLPR=   2.000 %
       DIODE AREA CHECK:       DAPER=    5.000 %
       UNSPECIFIED SCHEMATICAL PARAMETERS ARE CONSIDERED AS MISMATCH
       UNSPECIFIED LAYOUT PARAMETERS ARE CONSIDERED AS MISMATCH
1      ***************************************************
       *********   CORRESPONDENCE NODE PAIRS   **********
       ***************************************************


         SCHEMATICS                LAYOUT              PAD TYPE

  AGND                  67   AGND                  74   G
  ALGND                 68   ALGND                100   G
  ALVDD                 69   ALVDD                679   P
  AVDD                  70   AVDD                  82   P
  DGND                  71   DGND                  14   G
  DVDD                  72   DVDD                  13   P
  OGND                  73   OGND                   6   G
  OVDD                  74   OVDD                   5   P
  PGND                  75   PGND                  27   G
  PVDD                  76   PVDD                  28   P
  SUBGND                77   SUBGND                 4   G
  green_out_b<0>        32   green_out_b<0>     12638   I
  green_out_a<6>        18   green_out_a<6>     16152   I
  green_out_a<5>        19   green_out_a<5>     16128   I
  green_out_a<4>        20   green_out_a<4>     16097   I
  green_out_a<3>        21   green_out_a<3>     16016   I
  green_out_a<2>        22   green_out_a<2>     15920   I
  green_out_a<1>        23   green_out_a<1>     15731   I
  green_out_a<0>        24   green_out_a<0>     15709   I
  green_out_b<7>        25   green_out_b<7>     15195   I
  blue_out_b<7>          9   blue_out_b<7>       3933   I
  blue_out_b<6>         10   blue_out_b<6>       2842   I
  green_out_b<1>        31   green_out_b<1>     12685   I
  blue_out_b<5>         11   blue_out_b<5>       1719   I
  blue_out_b<4>         12   blue_out_b<4>        695   I
  blue_out_b<3>         13   blue_out_b<3>        626   I
  blue_out_b<2>         14   blue_out_b<2>        450   I
  blue_out_b<1>         15   blue_out_b<1>        162   I
  blue_out_b<0>         16   blue_out_b<0>         67   I
  blue_out_a<7>          1   blue_out_a<7>      12320   O
  blue_out_a<6>          2   blue_out_a<6>      12286   I
  blue_out_a<5>          3   blue_out_a<5>      12112   I
  blue_out_a<4>          4   blue_out_a<4>      11150   I
```

green_out_b<2>	30	green_out_b<2>	12704	I
blue_out_a<3>	5	blue_out_a<3>	10037	I
blue_out_a<2>	6	blue_out_a<2>	9114	I
blue_out_a<1>	7	blue_out_a<1>	8189	I
blue_out_a<0>	8	blue_out_a<0>	7412	I
green_out_b<3>	29	green_out_b<3>	12736	I
green_out_b<4>	28	green_out_b<4>	13007	I
green_out_b<5>	27	green_out_b<5>	13841	I
green_out_b<6>	26	green_out_b<6>	14560	I
pad_XFILT_OUT	82	pad_XFILT_OUT	29	I
green_out_a<7>	17	green_out_a<7>	16177	I
pad_A0	83	pad_A0	6124	I
pad_A1	84	pad_A1	4847	I
pad_Bain	85	pad_Bain	12569	I
pad_Bclampv	86	pad_Bclampv	12490	I
pad_Bmidsc	33	pad_Bmidsc	12626	I
pad_CKEXT	78	pad_CKEXT	398	I
pad_CTL0	34	pad_CTL0	8	I
pad_CTL1	35	pad_CTL1	9	I
pad_CTL2	36	pad_CTL2	10	I
pad_CTL3_MCL	37	pad_CTL3_MCL	11	I
pad_Ckinv	87	pad_Ckinv	9820	I
pad_Coast	88	pad_Coast	553	I
pad_DE	38	pad_DE	16330	I
pad_DVISCL	89	pad_DVISCL	26	I
pad_DVISDA	39	pad_DVISDA	25	I
pad_GClampv	90	pad_GClampv	15666	I
pad_Gain	91	pad_Gain	15701	I
pad_Gmidsc	40	pad_Gmidsc	15707	I
pad_Hsync	92	pad_Hsync	144	I
pad_MDA	41	pad_MDA	24	I
pad_REFOUT	42	pad_REFOUT	16494	I
pad_Rain	93	pad_Rain	16193	I
pad_Rclampv	94	pad_Rclampv	16176	I
pad_Refin	95	pad_Refin	16495	I
pad_Rmidsc	43	pad_Rmidsc	16488	I
pad_Rterm	96	pad_Rterm	15	I
pad_Rx0n	97	pad_Rx0n	21	I
pad_Rx0p	98	pad_Rx0p	20	I
pad_Rx1n	99	pad_Rx1n	19	I
pad_Rx1p	100	pad_Rx1p	18	I
pad_Rx2n	101	pad_Rx2n	17	I
pad_Rx2p	102	pad_Rx2p	16	I
pad_Rxcn	79	pad_Rxcn	23	I
pad_Rxcp	80	pad_Rxcp	22	I
pad_SCANclk	103	pad_SCANclk	12	I
pad_SCANin	104	pad_SCANin	16493	I
pad_SCANout	44	pad_SCANout	7	I
pad_SCL	105	pad_SCL	7232	I
pad_SDA	81	pad_SDA	8073	I
pad_SOGIN	106	pad_SOGIN	15061	I
pad_SOGOUT	45	pad_SOGOUT	16328	I
pad_SyncDT	46	pad_SyncDT	16331	I
pad_VSOUT	47	pad_VSOUT	16329	I
pad_Vsync	107	pad_Vsync	57	I
pad_clamp	108	pad_clamp	8931	I
pad_datack	48	pad_datack	16333	I
pad_datackb	49	pad_datackb	16332	I
pad_hsout	50	pad_hsout	16326	I
red_out_a<0>	58	red_out_a<0>	16318	I

red_out_a<1>	57	red_out_a<1>	16319	I
red_out_a<2>	56	red_out_a<2>	16320	I
red_out_a<3>	55	red_out_a<3>	16321	I
red_out_a<4>	54	red_out_a<4>	16322	I
red_out_a<5>	53	red_out_a<5>	16323	I
red_out_a<6>	52	red_out_a<6>	16324	I
red_out_a<7>	51	red_out_a<7>	16325	I
red_out_b<0>	66	red_out_b<0>	16327	I
red_out_b<1>	65	red_out_b<1>	16311	I
red_out_b<2>	64	red_out_b<2>	16312	I
red_out_b<3>	63	red_out_b<3>	16313	I
red_out_b<4>	62	red_out_b<4>	16314	I
red_out_b<5>	61	red_out_b<5>	16315	I
red_out_b<6>	60	red_out_b<6>	16316	I
red_out_b<7>	59	red_out_b<7>	16317	I

```
***TOTAL =      108***
.. BIG SCH NODE : AGND              67 CONN =    778
.. BIG SCH NODE : ALGND             68 CONN =    420
.. BIG SCH NODE : ALVDD             69 CONN =    215
.. BIG SCH NODE : AVDD              70 CONN =    715
.. BIG SCH NODE : DGND              71 CONN =    465
.. BIG SCH NODE : DVDD              72 CONN =    353
.. BIG SCH NODE : PVDD              76 CONN =    157
** WARNING ** UN-LABELED BIG SCH NODE =    110 CONN =    235
** WARNING ** UN-LABELED BIG SCH NODE =    133 CONN =    389
** WARNING ** UN-LABELED BIG SCH NODE =    247 CONN =    209
** WARNING ** UN-LABELED BIG SCH NODE =    272 CONN =   2657
** WARNING ** UN-LABELED BIG SCH NODE =    295 CONN =    290
** WARNING ** UN-LABELED BIG SCH NODE =    296 CONN =    298
** WARNING ** UN-LABELED BIG SCH NODE =  28259 CONN =    305
** WARNING ** UN-LABELED BIG SCH NODE =  28260 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28261 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28262 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28264 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28265 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28266 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28279 CONN =    581
.. BIG LAY NODE : DVDD              13 CONN =    353
.. BIG LAY NODE : DGND              14 CONN =    465
.. BIG LAY NODE : PVDD              28 CONN =    157
** WARNING ** UN-LABELED BIG LAY NODE =     48 CONN =    209
** WARNING ** UN-LABELED BIG LAY NODE =     53 CONN =    290
.. BIG LAY NODE : AGND              74 CONN =    778
.. BIG LAY NODE : AVDD              82 CONN =    715
.. BIG LAY NODE : ALGND            100 CONN =    420
** WARNING ** UN-LABELED BIG LAY NODE =    237 CONN =   2657
** WARNING ** UN-LABELED BIG LAY NODE =    546 CONN =    235
.. BIG LAY NODE : ALVDD            679 CONN =    215
** WARNING ** UN-LABELED BIG LAY NODE =    777 CONN =    389
** WARNING ** UN-LABELED BIG LAY NODE =   1614 CONN =    338
** WARNING ** UN-LABELED BIG LAY NODE =   1616 CONN =    338
** WARNING ** UN-LABELED BIG LAY NODE =   1637 CONN =    338
** WARNING ** UN-LABELED BIG LAY NODE =   4641 CONN =    305
** WARNING ** UN-LABELED BIG LAY NODE =   7388 CONN =    206
** WARNING ** UN-LABELED BIG LAY NODE =   7567 CONN =    298
** WARNING ** UN-LABELED BIG LAY NODE =   7767 CONN =    206
** WARNING ** UN-LABELED BIG LAY NODE =   8038 CONN =    206
** WARNING ** UN-LABELED BIG LAY NODE =   8397 CONN =    581

NUMBER OF VALID CORRESPONDENCE NODE PAIRS =       97
```

1 **********
********** LVS DEVICE MATCH SUMMARY **********
****

NUMBER OF UN-MATCHED SCHEMATICS DEVICES = 0
NUMBER OF UN-MATCHED LAYOUT DEVICES = 349
NUMBER OF MATCHED SCHEMATICS DEVICES = 136213
NUMBER OF MATCHED LAYOUT DEVICES = 136213

1 **********
********** DISCREPANCY POINTS LISTING **********
****

NO DISCREPANCIES

****
******** DEVICE MATCHING SUMMARY BY TYPE ********
****

TYPE	SUB-TYPE	TOTAL DEVICE SCH.	TOTAL DEVICE LAY.	UN-MATCHED DEVICE SCH.	UN-MATCHED DEVICE LAY.
MOS	P	122675	122676	0	1
MOS	N	123029	123029	0	0
BJT	SP	168	168	0	0
RES	P	247	247	0	0
RES	M1	1	1	0	0
RES	N	403	403	0	0
RES	M3	6	6	0	0
RES	NW	9	9	0	0
RES	P1	12	12	0	0
RES	P2	4	4	0	0
RES	B1	24	24	0	0
DIO	ND	96	96	0	0
CAP	N	21	21	0	0
CAP	PW	32	32	0	0
CAP	PS	170	518	0	348

1 **********
********** UN-MATCHED LAYOUT DEVICES **********
********** (LIST UP TO 100) **********
****

: ?DEV109742 MOS P
: X=1209.28 Y=-475.68
?94371, ?77583, ?77584
W = 23.55 L = 5.00
: ?DEV84 CAP PS
: X=-105.55 Y=-148.10
?94738, ?94390

: ?DEV85 CAP PS
: X=-84.05 Y=-148.10
?94739, ?94391
: ?DEV86 CAP PS
: X=-62.55 Y=-148.10
?94740, ?94392
: ?DEV87 CAP PS
: X=-41.05 Y=-148.10
?94741, ?94393
: ?DEV107 CAP PS
: X=-105.55 Y=-133.40
?94756, ?94408
: ?DEV110 CAP PS
: X=-41.05 Y=-133.40
?94757, ?94409
: ?DEV126 CAP PS
: X=-105.55 Y=-118.70
?94768, ?94420
: ?DEV129 CAP PS
: X=-41.05 Y=-118.70
?94769, ?94421
: ?DEV145 CAP PS
: X=-105.55 Y=-104.00
?94780, ?94432
: ?DEV148 CAP PS
: X=-41.05 Y=-104.00
?94781, ?94433
: ?DEV164 CAP PS
: X=-105.55 Y=-89.30
?94792, ?94444
: ?DEV167 CAP PS
: X=-41.05 Y=-89.25
?94793, ?94445
: ?DEV195 CAP PS
: X=-105.55 Y=-74.60
?94805, ?94457
: ?DEV196 CAP PS
: X=-84.05 Y=-74.60
?94806, ?94458
: ?DEV197 CAP PS
: X=-62.55 Y=-74.60
?94807, ?94459
: ?DEV198 CAP PS
: X=-41.05 Y=-74.60
?94808, ?94460
: ?DEV278 CAP PS
: X=-105.60 Y=614.65
?94854, ?94506
: ?DEV279 CAP PS
: X=-84.10 Y=614.65
?94855, ?94507
: ?DEV280 CAP PS
: X=-62.60 Y=614.65
?94856, ?94508
: ?DEV281 CAP PS
: X=-41.10 Y=614.65
?94857, ?94509
: ?DEV301 CAP PS
: X=-105.60 Y=629.35
?94872, ?94524

```
: ?DEV304    CAP PS
:  X=-41.10      Y=629.35
   ?94873, ?94525
: ?DEV320    CAP PS
:  X=-105.60     Y=644.05
   ?94884, ?94536
: ?DEV323    CAP PS
:  X=-41.10      Y=644.05
   ?94885, ?94537
: ?DEV339    CAP PS
:  X=-105.60     Y=658.75
   ?94896, ?94548
: ?DEV342    CAP PS
:  X=-41.10      Y=658.75
   ?94897, ?94549
: ?DEV358    CAP PS
:  X=-105.60     Y=673.45
   ?94908, ?94560
: ?DEV361    CAP PS
:  X=-41.10      Y=673.50
   ?94909, ?94561
: ?DEV389    CAP PS
:  X=-105.60     Y=688.15
   ?94921, ?94573
: ?DEV390    CAP PS
:  X=-84.10      Y=688.15
   ?94922, ?94574
: ?DEV391    CAP PS
:  X=-62.60      Y=688.15
   ?94923, ?94575
: ?DEV392    CAP PS
:  X=-41.10      Y=688.15
   ?94924, ?94576
: ?DEV453    CAP PS
:  X=-103.33     Y=1382.80
   ?94970, ?94622
: ?DEV454    CAP PS
:  X=-81.83      Y=1382.80
   ?94971, ?94623
: ?DEV455    CAP PS
:  X=-60.33      Y=1382.80
   ?94972, ?94624
: ?DEV456    CAP PS
:  X=-38.83      Y=1382.80
   ?94973, ?94625
: ?DEV476    CAP PS
:  X=-103.33     Y=1397.50
   ?94988, ?94640
: ?DEV479    CAP PS
:  X=-38.83      Y=1397.50
   ?94989, ?94641
: ?DEV495    CAP PS
:  X=-103.33     Y=1412.20
   ?95000, ?94652
: ?DEV498    CAP PS
:  X=-38.83      Y=1412.20
   ?95001, ?94653
: ?DEV514    CAP PS
:  X=-103.33     Y=1426.90
   ?95012, ?94664
```



```
: ?DEV517    CAP PS
:  X=-38.83      Y=1426.90
   ?95013, ?94665
: ?DEV533    CAP PS
:  X=-103.33     Y=1441.60
   ?95024, ?94676
: ?DEV536    CAP PS
:  X=-38.83      Y=1441.65
   ?95025, ?94677
: ?DEV564    CAP PS
:  X=-103.33     Y=1456.30
   ?95037, ?94689
: ?DEV565    CAP PS
:  X=-81.83      Y=1456.30
   ?95038, ?94690
: ?DEV566    CAP PS
:  X=-60.33      Y=1456.30
   ?95039, ?94691
: ?DEV567    CAP PS
:  X=-38.83      Y=1456.30
   ?95040, ?94692
: ?DEV62     CAP PS
:  X=-427.75     Y=-162.80
   ?94720, ?94372
: ?DEV63     CAP PS
:  X=-406.35     Y=-162.80
   ?94721, ?94373
: ?DEV64     CAP PS
:  X=-384.95     Y=-162.80
   ?94722, ?94374
: ?DEV81     CAP PS
:  X=-427.75     Y=-148.10
   ?94735, ?94387
: ?DEV82     CAP PS
:  X=-406.35     Y=-148.10
   ?94736, ?94388
: ?DEV83     CAP PS
:  X=-384.95     Y=-148.10
   ?94737, ?94389
: ?DEV104    CAP PS
:  X=-427.75     Y=-133.40
   ?94754, ?94406
: ?DEV106    CAP PS
:  X=-384.95     Y=-133.40
   ?94755, ?94407
: ?DEV123    CAP PS
:  X=-427.75     Y=-118.70
   ?94766, ?94418
: ?DEV125    CAP PS
:  X=-384.95     Y=-118.70
   ?94767, ?94419
: ?DEV142    CAP PS
:  X=-427.75     Y=-104.00
   ?94778, ?94430
: ?DEV144    CAP PS
:  X=-384.95     Y=-104.00
   ?94779, ?94431
: ?DEV161    CAP PS
:  X=-427.75     Y=-89.30
   ?94790, ?94442
```

```
: ?DEV163    CAP PS
:  X=-384.95     Y=-89.30
   ?94791, ?94443
: ?DEV192    CAP PS
:  X=-427.75     Y=-74.60
   ?94802, ?94454
: ?DEV193    CAP PS
:  X=-406.35     Y=-74.60
   ?94803, ?94455
: ?DEV194    CAP PS
:  X=-384.95     Y=-74.60
   ?94804, ?94456
: ?DEV231    CAP PS
:  X=-427.75     Y=-59.90
   ?94821, ?94473
: ?DEV232    CAP PS
:  X=-406.35     Y=-59.90
   ?94822, ?94474
: ?DEV233    CAP PS
:  X=-384.95     Y=-59.90
   ?94823, ?94475
: ?DEV256    CAP PS
:  X=-427.80     Y=599.95
   ?94836, ?94488
: ?DEV257    CAP PS
:  X=-406.40     Y=599.95
   ?94837, ?94489
: ?DEV258    CAP PS
:  X=-385.00     Y=599.95
   ?94838, ?94490
: ?DEV275    CAP PS
:  X=-427.80     Y=614.65
   ?94851, ?94503
: ?DEV276    CAP PS
:  X=-406.40     Y=614.65
   ?94852, ?94504
: ?DEV277    CAP PS
:  X=-385.00     Y=614.65
   ?94853, ?94505
: ?DEV298    CAP PS
:  X=-427.80     Y=629.35
   ?94870, ?94522
: ?DEV300    CAP PS
:  X=-385.00     Y=629.35
   ?94871, ?94523
: ?DEV317    CAP PS
:  X=-427.80     Y=644.05
   ?94882, ?94534
: ?DEV319    CAP PS
:  X=-385.00     Y=644.05
   ?94883, ?94535
: ?DEV336    CAP PS
:  X=-427.80     Y=658.75
   ?94894, ?94546
: ?DEV338    CAP PS
:  X=-385.00     Y=658.75
   ?94895, ?94547
: ?DEV355    CAP PS
:  X=-427.80     Y=673.45
   ?94906, ?94558
```



```
: ?DEV357    CAP PS
:  X=-385.00     Y=673.45
   ?94907, ?94559
: ?DEV386    CAP PS
:  X=-427.80     Y=688.15
   ?94918, ?94570
: ?DEV387    CAP PS
:  X=-406.40     Y=688.15
   ?94919, ?94571
: ?DEV388    CAP PS
:  X=-385.00     Y=688.15
   ?94920, ?94572
: ?DEV405    CAP PS
:  X=-427.80     Y=702.85
   ?94937, ?94589
: ?DEV406    CAP PS
:  X=-406.40     Y=702.85
   ?94938, ?94590
: ?DEV407    CAP PS
:  X=-385.00     Y=702.85
   ?94939, ?94591
: ?DEV431    CAP PS
:  X=-425.53     Y=1368.10
   ?94952, ?94604
: ?DEV432    CAP PS
:  X=-404.13     Y=1368.10
   ?94953, ?94605
: ?DEV433    CAP PS
:  X=-382.73     Y=1368.10
   ?94954, ?94606
: ?DEV450    CAP PS
:  X=-425.53     Y=1382.80
   ?94967, ?94619
: ?DEV451    CAP PS
:  X=-404.13     Y=1382.80
   ?94968, ?94620
: ?DEV452    CAP PS
:  X=-382.73     Y=1382.80
   ?94969, ?94621
: ?DEV473    CAP PS
:  X=-425.53     Y=1397.50
   ?94986, ?94638
: ?DEV475    CAP PS
:  X=-382.73     Y=1397.50
   ?94987, ?94639
: ?DEV492    CAP PS
:  X=-425.53     Y=1412.20
   ?94998, ?94650
: ?DEV494    CAP PS
:  X=-382.73     Y=1412.20
   ?94999, ?94651
: ?DEV511    CAP PS
:  X=-425.53     Y=1426.90
   ?95010, ?94662

THE REST OF UN-MATCHED LAYOUT DEVICES ARE NOT LISTED

1      ************************************************
```

```
**********     LVS SUMMARY (REPEATED)    **********
*************************************************

*************************************************
**********    LVS DEVICE MATCH SUMMARY   **********
*************************************************


NUMBER OF UN-MATCHED SCHEMATICS DEVICES      =       0
NUMBER OF UN-MATCHED LAYOUT     DEVICES      =     349
NUMBER OF    MATCHED SCHEMATICS DEVICES      =  136213
NUMBER OF    MATCHED LAYOUT     DEVICES      =  136213

*************************************************
********  DEVICE MATCHING SUMMARY BY TYPE  ********
*************************************************
```

TYPE	SUB-TYPE	TOTAL DEVICE SCH.	TOTAL DEVICE LAY.	UN-MATCHED DEVICE SCH.	UN-MATCHED DEVICE LAY.
MOS	P	122675	122676	0	1
MOS	N	123029	123029	0	0
BJT	SP	168	168	0	0
RES	P	247	247	0	0
RES	M1	1	1	0	0
RES	N	403	403	0	0
RES	M3	6	6	0	0
RES	NW	9	9	0	0
RES	P1	12	12	0	0
RES	P2	4	4	0	0
RES	B1	24	24	0	0
DIO	ND	96	96	0	0
CAP	N	21	21	0	0
CAP	PW	32	32	0	0
CAP	PS	170	518	0	348

```
*************************************************
**/W* -- SCHEMATIC AND LAYOUT MAY NOT MATCH     **
**         CHECK ALL ABOVE DISCREPANCY          **
**         AND WARNING MESSAGES                 **
*************************************************
```

```
*********************************************************************
*/N*    DRACULA  ( REV. 4.7.03-2000 /   SUN-4 S5R4   /GENDATE: 29-FEB/2000  )
                  *** ( Copyright 1995, Cadence ) ***
*/N*     EXEC TIME =11:42:50       DATE =14-MAY-2002     HOSTNAME = firebird
*********************************************************************

 INDISK PRIMARY CELL : 9887A


 ********** LVSNET SUMMARY REPORT **********


  WEFFECT VALUE= 0.0000000


 ******* REDUCE (LAYOUT) SUMMARY REPORT *******


 ******* STATISTICS BEFORE REDUCE ****

       MOS      BJT      RES    DIODE      CAP      UND      BOX     CELL      LDD
    291615      195      961      163      315      348        0        0        0

  OPTION TO SMASH PARALLEL DEVICES IS -- ON
  OPTION TO CONSTRUCT MOS PARALLEL/SERIES STRUCTURES IS --  ON
  OPTION TO SMASH PSEUDO PARALLEL DEVICES IS --  ON
  OPTION TO FORM CMOS GATES IS --  ON
  OPTION TO EXTRACT SUBSTRATE NODES OF GATES IS -- OFF


 ******* STATISTICS AFTER REDUCE ****
       MOS      BJT      RES      INV    DIODE      CAP     SDWI     PDWI     SUPI
     58627      168      706    56627       96      223       63        0       58
      PUPI     SDW      PDW      SUP      PUP      AND       OR      AOI     NAND
       183      170      128      387      136      513     2521      384    11194
       NOR      OAI      UND      BOX     CELL      LDD     SMID     PMID   MOSCAP
      1929     1925      348        0        0        0        0        0      181
      DRAM     SRAM
         0        0

 ******* REDUCE (SCHEMATIC) SUMMARY REPORT *******


 ******* STATISTICS BEFORE REDUCE ****

       MOS      BJT      RES    DIODE      CAP      UND      BOX     CELL      LDD
    268800      191      807      115      264        0        0        0        0


 ******* STATISTICS AFTER REDUCE ****
       MOS      BJT      RES      INV    DIODE      CAP     SDWI     PDWI     SUPI
     58626      168      706    56627       96      223       63        0       58
      PUPI     SDW      PDW      SUP      PUP      AND       OR      AOI     NAND
       183      165      128      387      131      513     2521      384    11199
       NOR      OAI      UND      BOX     CELL      LDD     SMID     PMID   MOSCAP
      1929     1925        0        0        0        0        0        0      181
      DRAM     SRAM
         0        0
```



```
 */W* WARNING :: SOFT-CONNECTING MULTIPLE NODES IN UPPER LAYERS
          TO ONE POLYGON REGION IN: NPINT

 */I* # OF REGIONS MULTIPLE SOFT-CONNECTED  =          4


 */I* PLEASE REFER TO .ERC FILE FOR DETAIL


 */W* WARNING :: SOFT-CONNECTING MULTIPLE NODES IN UPPER LAYERS
          TO ONE POLYGON REGION IN: PPINT

 */I* # OF REGIONS MULTIPLE SOFT-CONNECTED  =        453


 */I* PLEASE REFER TO .ERC FILE FOR DETAIL



  ***************** LVS REPORT *****************

        DATE : 14-MAY-2002
        TIME : 12:21:21



        PRINTLINE      =     1000
        WPERCENT(MOS)  =    2.000 %
        LPERCENT(MOS)  =    1.000 %
        BJT EMITTER AREA CHECK: EMAPER=    5.000 %
        CAPACITOR VALUE CHECK:  CVPER=     5.000 %
        RESISTOR WIDTH CHECK:   RESWPR=    2.000 %
        RESISTOR LENGTH CHECK:  RESLPR=    2.000 %
        DIODE AREA CHECK:       DAPER=     5.000 %
        UNSPECIFIED SCHEMATICAL PARAMETERS ARE CONSIDERED AS MISMATCH
        UNSPECIFIED LAYOUT PARAMETERS ARE CONSIDERED AS MISMATCH
1       ***************************************************
        **********   CORRESPONDENCE NODE PAIRS   **********
        ***************************************************

          SCHEMATICS                LAYOUT              PAD TYPE

    AGND                  67   AGND                  74   G
    ALGND                 68   ALGND                100   G
    ALVDD                 69   ALVDD                679   P
    AVDD                  70   AVDD                  82   P
    DGND                  71   DGND                  14   G
    DVDD                  72   DVDD                  13   P
    OGND                  73   OGND                   6   G
    OVDD                  74   OVDD                   5   P
    PGND                  75   PGND                  27   G
    PVDD                  76   PVDD                  28   P
    SUBGND                77   SUBGND                 4   G
    green_out_b<0>        32   green_out_b<0>     12638   I
    green_out_a<6>        18   green_out_a<6>     16152   I
    green_out_a<5>        19   green_out_a<5>     16128   I
    green_out_a<4>        20   green_out_a<4>     16097   I
```

green_out_a<3>	21	green_out_a<3>	16016	I
green_out_a<2>	22	green_out_a<2>	15920	I
green_out_a<1>	23	green_out_a<1>	15731	I
green_out_a<0>	24	green_out_a<0>	15709	I
green_out_b<7>	25	green_out_b<7>	15195	I
blue_out_b<7>	9	blue_out_b<7>	3933	I
blue_out_b<6>	10	blue_out_b<6>	2842	I
green_out_b<1>	31	green_out_b<1>	12685	I
blue_out_b<5>	11	blue_out_b<5>	1719	I
blue_out_b<4>	12	blue_out_b<4>	695	I
blue_out_b<3>	13	blue_out_b<3>	626	I
blue_out_b<2>	14	blue_out_b<2>	450	I
blue_out_b<1>	15	blue_out_b<1>	162	I
blue_out_b<0>	16	blue_out_b<0>	67	I
blue_out_a<7>	1	blue_out_a<7>	12320	O
blue_out_a<6>	2	blue_out_a<6>	12286	I
blue_out_a<5>	3	blue_out_a<5>	12112	I
blue_out_a<4>	4	blue_out_a<4>	11150	I
green_out_b<2>	30	green_out_b<2>	12704	I
blue_out_a<3>	5	blue_out_a<3>	10037	I
blue_out_a<2>	6	blue_out_a<2>	9114	I
blue_out_a<1>	7	blue_out_a<1>	8189	I
blue_out_a<0>	8	blue_out_a<0>	7412	I
green_out_b<3>	29	green_out_b<3>	12736	I
green_out_b<4>	28	green_out_b<4>	13007	I
green_out_b<5>	27	green_out_b<5>	13841	I
green_out_b<6>	26	green_out_b<6>	14560	I
pad_XFILT_OUT	82	pad_XFILT_OUT	29	I
green_out_a<7>	17	green_out_a<7>	16177	I
pad_A0	83	pad_A0	6124	I
pad_A1	84	pad_A1	4847	I
pad_Bain	85	pad_Bain	12569	I
pad_Bclampv	86	pad_Bclampv	12490	I
pad_Bmidsc	33	pad_Bmidsc	12626	I
pad_CKEXT	78	pad_CKEXT	398	I
pad_CTL0	34	pad_CTL0	8	I
pad_CTL1	35	pad_CTL1	9	I
pad_CTL2	36	pad_CTL2	10	I
pad_CTL3_MCL	37	pad_CTL3_MCL	11	I
pad_Ckinv	87	pad_Ckinv	9820	I
pad_Coast	88	pad_Coast	553	I
pad_DE	38	pad_DE	16330	I
pad_DVISCL	89	pad_DVISCL	26	I
pad_DVISDA	39	pad_DVISDA	25	I
pad_GClampv	90	pad_GClampv	15666	I
pad_Gain	91	pad_Gain	15701	I
pad_Gmidsc	40	pad_Gmidsc	15707	I
pad_Hsync	92	pad_Hsync	144	I
pad_MDA	41	pad_MDA	24	I
pad_REFOUT	42	pad_REFOUT	16494	I
pad_Rain	93	pad_Rain	16193	I
pad_Rclampv	94	pad_Rclampv	16176	I
pad_Refin	95	pad_Refin	16495	I
pad_Rmidsc	43	pad_Rmidsc	16488	I
pad_Rterm	96	pad_Rterm	15	I
pad_Rx0n	97	pad_Rx0n	21	I
pad_Rx0p	98	pad_Rx0p	20	I
pad_Rx1n	99	pad_Rx1n	19	I
pad_Rx1p	100	pad_Rx1p	18	I
pad_Rx2n	101	pad_Rx2n	17	I

pad_Rx2p	102	pad_Rx2p	16	I
pad_Rxcn	79	pad_Rxcn	23	I
pad_Rxcp	80	pad_Rxcp	22	I
pad_SCANclk	103	pad_SCANclk	12	I
pad_SCANin	104	pad_SCANin	16493	I
pad_SCANout	44	pad_SCANout	7	I
pad_SCL	105	pad_SCL	7232	I
pad_SDA	81	pad_SDA	8073	I
pad_SOGIN	106	pad_SOGIN	15061	I
pad_SOGOUT	45	pad_SOGOUT	16328	I
pad_SyncDT	46	pad_SyncDT	16331	I
pad_VSOUT	47	pad_VSOUT	16329	I
pad_Vsync	107	pad_Vsync	57	I
pad_clamp	108	pad_clamp	8931	I
pad_datack	48	pad_datack	16333	I
pad_datackb	49	pad_datackb	16332	I
pad_hsout	50	pad_hsout	16326	I
red_out_a<0>	58	red_out_a<0>	16318	I
red_out_a<1>	57	red_out_a<1>	16319	I
red_out_a<2>	56	red_out_a<2>	16320	I
red_out_a<3>	55	red_out_a<3>	16321	I
red_out_a<4>	54	red_out_a<4>	16322	I
red_out_a<5>	53	red_out_a<5>	16323	I
red_out_a<6>	52	red_out_a<6>	16324	I
red_out_a<7>	51	red_out_a<7>	16325	I
red_out_b<0>	66	red_out_b<0>	16327	I
red_out_b<1>	65	red_out_b<1>	16311	I
red_out_b<2>	64	red_out_b<2>	16312	I
red_out_b<3>	63	red_out_b<3>	16313	I
red_out_b<4>	62	red_out_b<4>	16314	I
red_out_b<5>	61	red_out_b<5>	16315	I
red_out_b<6>	60	red_out_b<6>	16316	I
red_out_b<7>	59	red_out_b<7>	16317	I

TOTAL = 108

```
.. BIG SCH NODE : AGND              67 CONN =    778
.. BIG SCH NODE : ALGND             68 CONN =    420
.. BIG SCH NODE : ALVDD             69 CONN =    215
.. BIG SCH NODE : AVDD              70 CONN =    715
.. BIG SCH NODE : DGND              71 CONN =    465
.. BIG SCH NODE : DVDD              72 CONN =    353
.. BIG SCH NODE : PVDD              76 CONN =    157
** WARNING ** UN-LABELED BIG SCH NODE =    110 CONN =    235
** WARNING ** UN-LABELED BIG SCH NODE =    133 CONN =    389
** WARNING ** UN-LABELED BIG SCH NODE =    247 CONN =    209
** WARNING ** UN-LABELED BIG SCH NODE =    272 CONN =   2657
** WARNING ** UN-LABELED BIG SCH NODE =    295 CONN =    290
** WARNING ** UN-LABELED BIG SCH NODE =    296 CONN =    298
** WARNING ** UN-LABELED BIG SCH NODE =  28259 CONN =    305
** WARNING ** UN-LABELED BIG SCH NODE =  28260 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28261 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28262 CONN =    338
** WARNING ** UN-LABELED BIG SCH NODE =  28264 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28265 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28266 CONN =    206
** WARNING ** UN-LABELED BIG SCH NODE =  28279 CONN =    581
.. BIG LAY NODE : DVDD              13 CONN =    353
.. BIG LAY NODE : DGND              14 CONN =    465
.. BIG LAY NODE : PVDD              28 CONN =    157
** WARNING ** UN-LABELED BIG LAY NODE =     48 CONN =    209
** WARNING ** UN-LABELED BIG LAY NODE =     53 CONN =    290
```

```
  .. BIG LAY NODE : AGND              74 CONN =    778
  .. BIG LAY NODE : AVDD              82 CONN =    715
  .. BIG LAY NODE : ALGND            100 CONN =    420
  ** WARNING ** UN-LABELED BIG LAY NODE =    237 CONN =   2657
  ** WARNING ** UN-LABELED BIG LAY NODE =    546 CONN =    235
  .. BIG LAY NODE : ALVDD            679 CONN =    215
  ** WARNING ** UN-LABELED BIG LAY NODE =    777 CONN =    389
  ** WARNING ** UN-LABELED BIG LAY NODE =   1614 CONN =    338
  ** WARNING ** UN-LABELED BIG LAY NODE =   1616 CONN =    338
  ** WARNING ** UN-LABELED BIG LAY NODE =   1637 CONN =    338
  ** WARNING ** UN-LABELED BIG LAY NODE =   4641 CONN =    305
  ** WARNING ** UN-LABELED BIG LAY NODE =   7388 CONN =    206
  ** WARNING ** UN-LABELED BIG LAY NODE =   7567 CONN =    298
  ** WARNING ** UN-LABELED BIG LAY NODE =   7767 CONN =    206
  ** WARNING ** UN-LABELED BIG LAY NODE =   8038 CONN =    206
  ** WARNING ** UN-LABELED BIG LAY NODE =   8397 CONN =    581

  NUMBER OF VALID CORRESPONDENCE NODE PAIRS =       97

1       ************************************************
        **********    LVS DEVICE MATCH SUMMARY   **********
        ************************************************



    NUMBER OF UN-MATCHED SCHEMATICS DEVICES      =       5
    NUMBER OF UN-MATCHED LAYOUT     DEVICES      =     359
    NUMBER OF    MATCHED SCHEMATICS DEVICES      =  136208
    NUMBER OF    MATCHED LAYOUT     DEVICES      =  136208
1       ************************************************
        **********  DISCREPANCY POINTS  LISTING  **********
        ************************************************



 *************************** DISCREPANCY     1 ********************************

 --- NODE Xi144-Xi1-Xi8-i6_CO
 ---WITH UN-MATCHED DEVICES-------

 OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi7-U21_OUT
 ?DEV296576 NAND                    :         ***** UN-MATCHED *****
  Xi144-Xi1-Xi8-Xi7-U21_OUT,
  Xi144-Xi1-Xi8-i6_CO,
  Xi144-Xi1-Xi8-Xi7-U27_IN2
 OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi7-U28_IN2

  DEV308789 NOR                     :  DEV332996 NOR
                                    :  X=1051.40     Y=-1068.10
  Xi144-Xi1-Xi8-Xi7-U28_IN2,           Xi144-Xi1-Xi8-Xi7-U28_IN2,
  Xi144-Xi1-Xi8-Xi7-U27_IN2,           Xi144-Xi1-Xi8-Xi7-U27_IN2,
  Xi144-Xi1-Xi8-i6_CO                  Xi144-Xi1-Xi8-i6_CO
 OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi6-U28_OUT

  DEV308791 NOR                     :  DEV332999 NOR
                                    :  X=1056.90     Y=-1061.20
  Xi144-Xi1-Xi8-Xi6-U28_OUT,           Xi144-Xi1-Xi8-Xi6-U28_OUT,
  Xi144-Xi1-Xi8-Xi6-U28_IN2,           Xi144-Xi1-Xi8-i6_CO,
  Xi144-Xi1-Xi8-i6_CO                  Xi144-Xi1-Xi8-Xi6-U28_IN2
 OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi34-INCPB
```



```
  DEV321125 INV                     :  DEV339343 INV
                                    :  X=1075.20     Y=-1112.25
  Xi144-Xi1-Xi8-Xi34-INCPB,            Xi144-Xi1-Xi8-Xi34-INCPB,
  Xi144-Xi1-Xi8-i6_CO                  Xi144-Xi1-Xi8-i6_CO
 OCCURRENCE NAME  Xi144-Xi1-Xi8-i6_CO

  DEV339528 INV                     :  DEV362706 INV
                                    :  X=1048.70     Y=-1063.40
  Xi144-Xi1-Xi8-i6_CO,                 Xi144-Xi1-Xi8-i6_CO,
  Xi144-Xi1-Xi8-Xi6-U21_OUT            Xi144-Xi1-Xi8-Xi6-U21_OUT
         ***** UN-MATCHED *****     :  ?DEV294765 SDW
                                    :  X=1047.30     Y=-1068.10
                                       Xi144-Xi1-Xi8-Xi7-U21_OUT,
                                       Xi144-Xi1-Xi8-i6_CO,
                                       Xi144-Xi1-Xi8-Xi7-U27_IN2
         ***** UN-MATCHED *****     :  ?DEV318894 PUP
                                    :  X=1047.40     Y=-1073.70
                                       ?54765, Xi144-Xi1-Xi8-i6_CO,
                                       Xi144-Xi1-Xi8-Xi7-U27_IN2

*************************** DISCREPANCY     2 ********************************

--- NODE Xi144-Xi1-Xi8-i3_CO
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U21_OUT
?DEV296584 NAND                     :         ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi4-U21_OUT,
 Xi144-Xi1-Xi8-i3_CO,
 Xi144-Xi1-Xi8-Xi4-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi3-U28_OUT

 DEV308794 NOR                      :  DEV333002 NOR
                                    :  X=1105.90     Y=-1036.20
 Xi144-Xi1-Xi8-Xi3-U28_OUT,            Xi144-Xi1-Xi8-Xi3-U28_OUT,
 Xi144-Xi1-Xi8-Xi3-U28_IN2,            Xi144-Xi1-Xi8-i3_CO,
 Xi144-Xi1-Xi8-i3_CO                   Xi144-Xi1-Xi8-Xi3-U28_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U28_IN2

 DEV308801 NOR                      :  DEV333005 NOR
                                    :  X=1101.80     Y=-1043.10
 Xi144-Xi1-Xi8-Xi4-U28_IN2,            Xi144-Xi1-Xi8-Xi4-U28_IN2,
 Xi144-Xi1-Xi8-Xi4-U27_IN2,            Xi144-Xi1-Xi8-Xi4-U27_IN2,
 Xi144-Xi1-Xi8-i3_CO                   Xi144-Xi1-Xi8-i3_CO
OCCURRENCE NAME  Xi144-Xi1-Xi8-i3_CO

 DEV339529 INV                      :  DEV362709 INV
                                    :  X=1097.70     Y=-1038.40
 Xi144-Xi1-Xi8-i3_CO,                  Xi144-Xi1-Xi8-i3_CO,
 Xi144-Xi1-Xi8-Xi3-U21_OUT             Xi144-Xi1-Xi8-Xi3-U21_OUT
        ***** UN-MATCHED *****      :  ?DEV294767 SDW
                                    :  X=1097.70     Y=-1043.10
                                       Xi144-Xi1-Xi8-Xi4-U21_OUT,
                                       Xi144-Xi1-Xi8-i3_CO,
                                       Xi144-Xi1-Xi8-Xi4-U27_IN2
        ***** UN-MATCHED *****      :  ?DEV318914 PUP
                                    :  X=1097.80     Y=-1048.70
                                       ?55949, Xi144-Xi1-Xi8-i3_CO,
                                       Xi144-Xi1-Xi8-Xi4-U27_IN2
```

```
************************** DISCREPANCY     3 ********************************

--- NODE Xi144-Xi1-Xi8-i4_CO
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi5-U21_OUT
?DEV296582 NAND                  :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi5-U21_OUT,
 Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-Xi5-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi5-U28_IN2

 DEV308798 NOR                   :  DEV333003 NOR
                                 :  X=1058.40     Y=-1043.10
 Xi144-Xi1-Xi8-Xi5-U28_IN2,         Xi144-Xi1-Xi8-Xi5-U28_IN2,
 Xi144-Xi1-Xi8-Xi5-U27_IN2,         Xi144-Xi1-Xi8-Xi5-U27_IN2,
 Xi144-Xi1-Xi8-i4_CO                Xi144-Xi1-Xi8-i4_CO
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U28_OUT

 DEV308800 NOR                   :  DEV333006 NOR
                                 :  X=1104.50     Y=-1043.10
 Xi144-Xi1-Xi8-Xi4-U28_OUT,         Xi144-Xi1-Xi8-Xi4-U28_OUT,
 Xi144-Xi1-Xi8-Xi4-U28_IN2,         Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-i4_CO                Xi144-Xi1-Xi8-Xi4-U28_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-i4_CO

 DEV339531 INV                   :  DEV362708 INV
                                 :  X=1096.30     Y=-1043.10
 Xi144-Xi1-Xi8-i4_CO,               Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-Xi4-U21_OUT          Xi144-Xi1-Xi8-Xi4-U21_OUT
        ***** UN-MATCHED *****    : ?DEV294766 SDW
                                 :  X=1054.30     Y=-1043.10
                                    Xi144-Xi1-Xi8-Xi5-U21_OUT,
                                    Xi144-Xi1-Xi8-i4_CO,
                                    Xi144-Xi1-Xi8-Xi5-U27_IN2
        ***** UN-MATCHED *****    : ?DEV318913 PUP
                                 :  X=1054.40     Y=-1048.70
                                    ?55948, Xi144-Xi1-Xi8-i4_CO,
                                    Xi144-Xi1-Xi8-Xi5-U27_IN2

************************** DISCREPANCY     4 ********************************

--- NODE Xi144-Xi1-Xi8-Xi7-U27_IN2
---WITH UN-MATCHED DEVICES-------


 DEV259520 MOS P  ----    Xi144-Xi1-Xi :  DEV65593  MOS P
  8-Xi7-mM39                     :  X=1074.00     Y=-1073.75
 Xi144-Xi1-Xi8-Xi7-INCP,            Xi144-Xi1-Xi8-Xi7-INCP,
 Xi144-Xi1-Xi8-Xi7-U27_IN2,         Xi144-Xi1-Xi8-Xi7-U27_IN2,
 Xi144-Xi1-Xi8-Xi7-U12_IN2          Xi144-Xi1-Xi8-Xi7-U12_IN2

 DEV259498 MOS N  ----    Xi144-Xi1-Xi :  DEV213928 MOS N
  8-Xi7-mM40                     :  X=1074.25     Y=-1068.75
 Xi144-Xi1-Xi8-Xi7-INCPB,           Xi144-Xi1-Xi8-Xi7-INCPB,
 Xi144-Xi1-Xi8-Xi7-U27_IN2,         Xi144-Xi1-Xi8-Xi7-U27_IN2,
 Xi144-Xi1-Xi8-Xi7-U12_IN2          Xi144-Xi1-Xi8-Xi7-U12_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi7-U21_OUT
?DEV296576 NAND                  :          ***** UN-MATCHED *****
```



```
 Xi144-Xi1-Xi8-Xi7-U21_OUT,
 Xi144-Xi1-Xi8-i6_CO,
 Xi144-Xi1-Xi8-Xi7-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi7-U27_IN2

 DEV296577 INV                   :  DEV320284 INV
                                 :  X=1080.10     Y=-1067.75
 Xi144-Xi1-Xi8-Xi7-U27_IN2,         Xi144-Xi1-Xi8-Xi7-U27_IN2,
 Xi144-Xi1-Xi8-Xi7-U30_IN           Xi144-Xi1-Xi8-Xi7-U30_IN
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi7-U28_IN2

 DEV308789 NOR                   :  DEV332996 NOR
                                 :  X=1051.40     Y=-1068.10
 Xi144-Xi1-Xi8-Xi7-U28_IN2,         Xi144-Xi1-Xi8-Xi7-U28_IN2,
 Xi144-Xi1-Xi8-Xi7-U27_IN2,         Xi144-Xi1-Xi8-Xi7-U27_IN2,
 Xi144-Xi1-Xi8-i6_CO                Xi144-Xi1-Xi8-i6_CO
OCCURRENCE NAME  Xi144-Xi1-Xi8-i7_QN

 DEV331132 INV                   :  DEV354683 INV
                                 :  X=1085.15     Y=-1068.10
 Xi144-Xi1-Xi8-i7_QN,               Xi144-Xi1-Xi8-i7_QN,
 Xi144-Xi1-Xi8-Xi7-U27_IN2          Xi144-Xi1-Xi8-Xi7-U27_IN2
        ***** UN-MATCHED *****    : ?DEV294765 SDW
                                 :  X=1047.30     Y=-1068.10
                                    Xi144-Xi1-Xi8-Xi7-U21_OUT,
                                    Xi144-Xi1-Xi8-i6_CO,
                                    Xi144-Xi1-Xi8-Xi7-U27_IN2
        ***** UN-MATCHED *****    : ?DEV318894 PUP
                                 :  X=1047.40     Y=-1073.70
                                    ?54765, Xi144-Xi1-Xi8-i6_CO,
                                    Xi144-Xi1-Xi8-Xi7-U27_IN2

************************** DISCREPANCY     5 ********************************

--- NODE Xi144-Xi1-Xi8-Xi7-U21_OUT
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi7-U21_OUT
?DEV296576 NAND                  :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi7-U21_OUT,
 Xi144-Xi1-Xi8-i6_CO,
 Xi144-Xi1-Xi8-Xi7-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-i7_CO

 DEV339527 INV                   :  DEV362705 INV
                                 :  X=1045.90     Y=-1068.10
 Xi144-Xi1-Xi8-i7_CO,               Xi144-Xi1-Xi8-i7_CO,
 Xi144-Xi1-Xi8-Xi7-U21_OUT          Xi144-Xi1-Xi8-Xi7-U21_OUT
        ***** UN-MATCHED *****    : ?DEV294765 SDW
                                 :  X=1047.30     Y=-1068.10
                                    Xi144-Xi1-Xi8-Xi7-U21_OUT,
                                    Xi144-Xi1-Xi8-i6_CO,
                                    Xi144-Xi1-Xi8-Xi7-U27_IN2

************************** DISCREPANCY     6 ********************************

--- NODE Xi144-Xi1-Xi8-Xi5-U27_IN2
---WITH UN-MATCHED DEVICES-------
```

```
 DEV259652 MOS P  ----     Xi144-Xi1-Xi :  DEV67648  MOS P
  8-Xi5-mM39                            :  X=1081.00     Y=-1048.75
 Xi144-Xi1-Xi8-Xi5-INCP,                   Xi144-Xi1-Xi8-Xi5-INCP,
 Xi144-Xi1-Xi8-Xi5-U27_IN2,                Xi144-Xi1-Xi8-Xi5-U27_IN2,
 Xi144-Xi1-Xi8-Xi5-U12_IN2                 Xi144-Xi1-Xi8-Xi5-U12_IN2

 DEV259630 MOS N  ----     Xi144-Xi1-Xi :  DEV215930 MOS N
  8-Xi5-mM40                            :  X=1081.25     Y=-1043.75
 Xi144-Xi1-Xi8-Xi5-INCPB,                  Xi144-Xi1-Xi8-Xi5-INCPB,
 Xi144-Xi1-Xi8-Xi5-U27_IN2,                Xi144-Xi1-Xi8-Xi5-U27_IN2,
 Xi144-Xi1-Xi8-Xi5-U12_IN2                 Xi144-Xi1-Xi8-Xi5-U12_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi5-U21_OUT
?DEV296582 NAND                         :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi5-U21_OUT,
 Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-Xi5-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi5-U27_IN2

 DEV296583 INV                          :  DEV320286 INV
                                        :  X=1087.10     Y=-1042.75
 Xi144-Xi1-Xi8-Xi5-U27_IN2,                Xi144-Xi1-Xi8-Xi5-U27_IN2,
 Xi144-Xi1-Xi8-Xi5-U30_IN                  Xi144-Xi1-Xi8-Xi5-U30_IN
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi5-U28_IN2

 DEV308798 NOR                          :  DEV333003 NOR
                                        :  X=1058.40     Y=-1043.10
 Xi144-Xi1-Xi8-Xi5-U28_IN2,                Xi144-Xi1-Xi8-Xi5-U28_IN2,
 Xi144-Xi1-Xi8-Xi5-U27_IN2,                Xi144-Xi1-Xi8-Xi5-U27_IN2,
 Xi144-Xi1-Xi8-i4_CO                       Xi144-Xi1-Xi8-i4_CO
OCCURRENCE NAME  Xi144-Xi1-Xi8-i5_QN

 DEV331138 INV                          :  DEV354687 INV
                                        :  X=1092.15     Y=-1043.10
 Xi144-Xi1-Xi8-i5_QN,                      Xi144-Xi1-Xi8-i5_QN,
 Xi144-Xi1-Xi8-Xi5-U27_IN2                 Xi144-Xi1-Xi8-Xi5-U27_IN2
        ***** UN-MATCHED *****          : ?DEV294766 SDW
                                        :  X=1054.30     Y=-1043.10
                                           Xi144-Xi1-Xi8-Xi5-U21_OUT,
                                           Xi144-Xi1-Xi8-i4_CO,
                                           Xi144-Xi1-Xi8-Xi5-U27_IN2
        ***** UN-MATCHED *****          : ?DEV318913 PUP
                                        :  X=1054.40     Y=-1048.70
                                           ?55948, Xi144-Xi1-Xi8-i4_CO,
                                           Xi144-Xi1-Xi8-Xi5-U27_IN2

************************** DISCREPANCY     7 ********************************

--- NODE Xi144-Xi1-Xi8-Xi5-U21_OUT
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi5-U21_OUT
?DEV296582 NAND                         :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi5-U21_OUT,
 Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-Xi5-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-i5_CO

 DEV339530 INV                          :  DEV362707 INV
                                        :  X=1052.90     Y=-1043.10
 Xi144-Xi1-Xi8-i5_CO,                      Xi144-Xi1-Xi8-i5_CO,
```



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 Xi144-Xi1-Xi8-Xi5-U21_OUT                 Xi144-Xi1-Xi8-Xi5-U21_OUT
        ***** UN-MATCHED *****          : ?DEV294766 SDW
                                        :  X=1054.30     Y=-1043.10
                                           Xi144-Xi1-Xi8-Xi5-U21_OUT,
                                           Xi144-Xi1-Xi8-i4_CO,
                                           Xi144-Xi1-Xi8-Xi5-U27_IN2

************************** DISCREPANCY     8 ********************************

--- NODE Xi144-Xi1-Xi8-Xi4-U27_IN2
---WITH UN-MATCHED DEVICES-------

 DEV259696 MOS P  ----     Xi144-Xi1-Xi :  DEV67649  MOS P
  8-Xi4-mM39                            :  X=1124.40     Y=-1048.75
 Xi144-Xi1-Xi8-Xi4-INCP,                   Xi144-Xi1-Xi8-Xi4-INCP,
 Xi144-Xi1-Xi8-Xi4-U27_IN2,                Xi144-Xi1-Xi8-Xi4-U27_IN2,
 Xi144-Xi1-Xi8-Xi4-U12_IN2                 Xi144-Xi1-Xi8-Xi4-U12_IN2

 DEV259674 MOS N  ----     Xi144-Xi1-Xi :  DEV215935 MOS N
  8-Xi4-mM40                            :  X=1124.65     Y=-1043.75
 Xi144-Xi1-Xi8-Xi4-INCPB,                  Xi144-Xi1-Xi8-Xi4-INCPB,
 Xi144-Xi1-Xi8-Xi4-U27_IN2,                Xi144-Xi1-Xi8-Xi4-U27_IN2,
 Xi144-Xi1-Xi8-Xi4-U12_IN2                 Xi144-Xi1-Xi8-Xi4-U12_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U21_OUT
?DEV296584 NAND                         :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi4-U21_OUT,
 Xi144-Xi1-Xi8-i3_CO,
 Xi144-Xi1-Xi8-Xi4-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U27_IN2

 DEV296585 INV                          :  DEV320287 INV
                                        :  X=1130.50     Y=-1042.75
 Xi144-Xi1-Xi8-Xi4-U27_IN2,                Xi144-Xi1-Xi8-Xi4-U27_IN2,
 Xi144-Xi1-Xi8-Xi4-U30_IN                  Xi144-Xi1-Xi8-Xi4-U30_IN
OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U28_IN2

 DEV308801 NOR                          :  DEV333005 NOR
                                        :  X=1101.80     Y=-1043.10
 Xi144-Xi1-Xi8-Xi4-U28_IN2,                Xi144-Xi1-Xi8-Xi4-U28_IN2,
 Xi144-Xi1-Xi8-Xi4-U27_IN2,                Xi144-Xi1-Xi8-Xi4-U27_IN2,
 Xi144-Xi1-Xi8-i3_CO                       Xi144-Xi1-Xi8-i3_CO
OCCURRENCE NAME  Xi144-Xi1-Xi8-i4_QN

 DEV331140 INV                          :  DEV354689 INV
                                        :  X=1135.55     Y=-1043.10
 Xi144-Xi1-Xi8-i4_QN,                      Xi144-Xi1-Xi8-i4_QN,
 Xi144-Xi1-Xi8-Xi4-U27_IN2                 Xi144-Xi1-Xi8-Xi4-U27_IN2
        ***** UN-MATCHED *****          : ?DEV294767 SDW
                                        :  X=1097.70     Y=-1043.10
                                           Xi144-Xi1-Xi8-Xi4-U21_OUT,
                                           Xi144-Xi1-Xi8-i3_CO,
                                           Xi144-Xi1-Xi8-Xi4-U27_IN2
        ***** UN-MATCHED *****          : ?DEV318914 PUP
                                        :  X=1097.80     Y=-1048.70
                                           ?55949, Xi144-Xi1-Xi8-i3_CO,
                                           Xi144-Xi1-Xi8-Xi4-U27_IN2

************************** DISCREPANCY     9 ********************************
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```
--- NODE Xi144-Xi1-Xi8-Xi4-U21_OUT
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi8-Xi4-U21_OUT
?DEV296584 NAND                    :        ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi4-U21_OUT,
 Xi144-Xi1-Xi8-i3_CO,
 Xi144-Xi1-Xi8-Xi4-U27_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi8-i4_CO

 DEV339531 INV                     :  DEV362708 INV
                                   :  X=1096.30     Y=-1043.10
 Xi144-Xi1-Xi8-i4_CO,              :     Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-Xi4-U21_OUT         :     Xi144-Xi1-Xi8-Xi4-U21_OUT
        ***** UN-MATCHED *****     : ?DEV294767 SDW
                                   :  X=1097.70     Y=-1043.10
                                   :     Xi144-Xi1-Xi8-Xi4-U21_OUT,
                                   :     Xi144-Xi1-Xi8-i3_CO,
                                   :     Xi144-Xi1-Xi8-Xi4-U27_IN2

************************** DISCREPANCY    10 *******************************

--- NODE Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi7-U16_OUT

 DEV294214 INV                     :  DEV302594 INV
                                   :  X=1768.60     Y=-1460.45
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi7-U16_OU  :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi7-U16_OU
 T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN    :  T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_OUT
?DEV296636 NAND                    :        ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
 OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
 , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
 7_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U28_IN2

 DEV308818 NOR                     :  DEV332993 NOR
                                   :  X=1776.00     Y=-1434.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U28_   :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U28_
 IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402   :  IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402
 -U27_IN2,                         :  -U27_IN2,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN  :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN

 DEV340470 NOR                     :  DEV321328 NOR
                                   :  X=1766.30     Y=-1434.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,       :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
 Xi144-Xi1-Xi0-Xi1-Xi27-mn1_D,           :  Xi144-Xi1-Xi0-Xi1-Xi27-mn1_D,
 Xi144-Xi1-Xi0-Xi1-Xi27-mn0_D            :  Xi144-Xi1-Xi0-Xi1-Xi27-mn0_D
        ***** UN-MATCHED *****     : ?DEV294754 SDW
                                   :  X=1771.90     Y=-1434.80
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
                                   :     OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
                                   :     , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
                                   :     7_IN2
        ***** UN-MATCHED *****     : ?DEV318374 PUP
                                   :  X=1772.00     Y=-1440.40
```



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                                   :     ?37481,
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
                                   :     IN2

************************** DISCREPANCY    11 *******************************

--- NODE Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OUT
?DEV296640 NAND                    :        ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
 T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U28_OUT

 DEV308820 NOR                     :  DEV332990 NOR
                                   :  X=1773.10     Y=-1458.90
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U28_OU   :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U28_OU
 T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U28   :  T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 _IN2,                             :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U28_IN
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO  :  2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U28_IN2

 DEV308824 NOR                     :  DEV332987 NOR
                                   :  X=1801.20     Y=-1459.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U28_IN   :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U28_IN
 2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27   :  2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27
 _IN2,                             :  _IN2,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO  :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO

 DEV339582 INV                     :  DEV362703 INV
                                   :  X=1764.90     Y=-1455.10
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,       :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U21_OU   :  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4-U21_OU
 T                                 :  T
        ***** UN-MATCHED *****     : ?DEV294753 SDW
                                   :  X=1797.10     Y=-1459.80
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
                                   :     T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
                                   :     2
        ***** UN-MATCHED *****     : ?DEV318343 PUP
                                   :  X=1797.20     Y=-1465.40
                                   :     ?36196,
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
                                   :     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
                                   :     2

************************** DISCREPANCY    12 *******************************

--- NODE Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_IN2
---WITH UN-MATCHED DEVICES-------

 DEV265363 MOS P  ----    Xi144-Xi1-Xi :  DEV33971  MOS P
  0-Xi1-Xi27-Xi0-Xi402-mM39          :  X=1798.60     Y=-1440.45
```

```
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-INCP    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-INCP
 , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2    , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
 7_IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4    7_IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4
 02-U12_IN2                               02-U12_IN2

 DEV265341 MOS N  ----    Xi144-Xi1-Xi :  DEV181484 MOS N
  0-Xi1-Xi27-Xi0-Xi402-mM40            :  X=1798.85     Y=-1435.45
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-INCP    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-INCP
 B, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U    B, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U
 27_IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi    27_IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi
 402-U12_IN2                              402-U12_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_OUT
?DEV296636 NAND                       :        ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
 OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
 , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
 7_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_IN2

 DEV296637 INV                         :  DEV320283 INV
                                       :  X=1804.70     Y=-1434.45
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
 IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402    IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402
 -U30_IN                                  -U30_IN
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U28_IN2

 DEV308818 NOR                         :  DEV332993 NOR
                                       :  X=1776.00     Y=-1434.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U28_    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U28_
 IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402    IN2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402
 -U27_IN2,                                -U27_IN2,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN         Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i402_QN

 DEV331153 INV                         :  DEV354681 INV
                                       :  X=1809.75     Y=-1434.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i402_QN,      Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i402_QN,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
 IN2                                      IN2
        ***** UN-MATCHED *****         : ?DEV294754 SDW
                                       :  X=1771.90     Y=-1434.80
                                          Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
                                          OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
                                          , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
                                          7_IN2
        ***** UN-MATCHED *****         : ?DEV318374 PUP
                                       :  X=1772.00     Y=-1440.40
                                          ?37481,
                                          Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
                                          Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
                                          IN2

************************** DISCREPANCY    13 ********************************

--- NODE Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_OUT
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_OUT
?DEV296636 NAND                       :        ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
```



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 OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
 , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
 7_IN2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i402_CO

 DEV339581 INV                         :  DEV362704 INV
                                       :  X=1770.50     Y=-1434.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i402_CO,      Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i402_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
 OUT                                      OUT
        ***** UN-MATCHED *****         : ?DEV294754 SDW
                                       :  X=1771.90     Y=-1434.80
                                          Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
                                          OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
                                          , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
                                          7_IN2

************************** DISCREPANCY    14 ********************************

--- NODE Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN2
---WITH UN-MATCHED DEVICES-------

 DEV265451 MOS P  ----    Xi144-Xi1-Xi :  DEV31651  MOS P
  0-Xi1-Xi27-Xi0-Xi5-mM39              :  X=1823.80     Y=-1465.45
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-INCP,     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-INCP,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U12    2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U12
 _IN2                                     _IN2

 DEV265429 MOS N  ----    Xi144-Xi1-Xi :  DEV179176 MOS N
  0-Xi1-Xi27-Xi0-Xi5-mM40              :  X=1824.05     Y=-1460.45
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-INCPB,    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-INCPB,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U12    2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U12
 _IN2                                     _IN2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OUT
?DEV296640 NAND                       :        ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
 T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN2

 DEV296641 INV                         :  DEV320281 INV
                                       :  X=1829.90     Y=-1459.45
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U30    2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U30
 _IN                                      _IN
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U28_IN2

 DEV308824 NOR                         :  DEV332987 NOR
                                       :  X=1801.20     Y=-1459.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U28_IN    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U28_IN
 2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27    2, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27
 _IN2,                                    _IN2,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO         Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i5_QN

 DEV331157 INV                         :  DEV354677 INV
```

```
                                    :  X=1834.95      Y=-1459.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i5_QN,     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i5_QN,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2                                      2
         ***** UN-MATCHED *****      : ?DEV294753 SDW
                                    :  X=1797.10      Y=-1459.80
                                       Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
                                       T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
                                       Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
                                       2
         ***** UN-MATCHED *****      : ?DEV318343 PUP
                                    :  X=1797.20      Y=-1465.40
                                       ?36196,
                                       Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
                                       Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
                                       2

************************** DISCREPANCY    15 ********************************

--- NODE Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OUT
---WITH UN-MATCHED DEVICES-------

OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OUT
?DEV296640 NAND                     :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
 T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2
OCCURRENCE NAME  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i5_CO

 DEV339583 INV                      :  DEV362702 INV
                                    :  X=1795.70      Y=-1459.80
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i5_CO,     Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i5_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
 T                                      T
         ***** UN-MATCHED *****      : ?DEV294753 SDW
                                    :  X=1797.10      Y=-1459.80
                                       Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
                                       T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
                                       Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
                                       2



        TOTAL        15 DISCREPANCY POINTS REPORTED



1       ***************************************************
        **********  DISCREPANCY POINTS  SUMMARY  **********
        ***************************************************

              15 MATCHED NODE TO UN-MATCHED LAYOUT AND SCHEMATIC DEVICES

        ***************************************************
        ********  DEVICE MATCHING SUMMARY BY TYPE  ********
        ***************************************************

    TYPE   SUB-TYPE            TOTAL DEVICE     UN-MATCHED DEVICE
```



```
                                SCH.       LAY.       SCH.       LAY.

    MOS       P               122675     122676         10         11
    MOS       N               123029     123029         10         10
    BJT       SP                 168        168          0          0
    RES       P                  247        247          0          0
    RES       M1                   1          1          0          0
    RES       N                  403        403          0          0
    RES       M3                   6          6          0          0
    RES       NW                   9          9          0          0
    RES       P1                  12         12          0          0
    RES       P2                   4          4          0          0
    RES       B1                  24         24          0          0
    DIO       ND                  96         96          0          0
    CAP       N                   21         21          0          0
    CAP       PW                  32         32          0          0
    CAP       PS                 170        170          0          0
1       ***************************************************
        **********  UN-MATCHED SCHEMATIC DEVICES **********
        **********     (LIST UP TO     100 )     **********
        ***************************************************

OCCURRENCE NAME Xi144-Xi1-Xi8-Xi7-U21_OUT
?DEV296576 NAND                     :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi7-U21_OUT,
 Xi144-Xi1-Xi8-i6_CO,
 Xi144-Xi1-Xi8-Xi7-U27_IN2
?DEV259483 MOS N  ----    Xi144-Xi1-Xi : ***** UN-MATCHED *****
  8-Xi7-mM9                         :
 Xi144-Xi1-Xi8-i6_CO, DGND,
 ?Xi144-Xi1-Xi8-Xi7-U21_U7_DRAIN
    W = 1.20        L = .35
?DEV259506 MOS P  ----    Xi144-Xi1-Xi : ***** UN-MATCHED *****
  8-Xi7-mM12                        :
 Xi144-Xi1-Xi8-i6_CO, DVDD,
 Xi144-Xi1-Xi8-Xi7-U21_OUT
    W = 3.00        L = .35
?DEV259484 MOS N  ----    Xi144-Xi1-Xi : ***** UN-MATCHED *****
  8-Xi7-mM10                        :
 Xi144-Xi1-Xi8-Xi7-U27_IN2,
 ?Xi144-Xi1-Xi8-Xi7-U21_U7_DRAIN,
 Xi144-Xi1-Xi8-Xi7-U21_OUT
    W = 1.20        L = .35
?DEV259505 MOS P  ----    Xi144-Xi1-Xi : ***** UN-MATCHED *****
  8-Xi7-mM11                        :
 Xi144-Xi1-Xi8-Xi7-U27_IN2, DVDD,
 Xi144-Xi1-Xi8-Xi7-U21_OUT
    W = 3.00        L = .35
OCCURRENCE NAME Xi144-Xi1-Xi8-Xi5-U21_OUT
?DEV296582 NAND                     :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi5-U21_OUT,
 Xi144-Xi1-Xi8-i4_CO,
 Xi144-Xi1-Xi8-Xi5-U27_IN2
?DEV259615 MOS N  ----    Xi144-Xi1-Xi : ***** UN-MATCHED *****
  8-Xi5-mM9                         :
 Xi144-Xi1-Xi8-i4_CO, DGND,
 ?Xi144-Xi1-Xi8-Xi5-U21_U7_DRAIN
    W = 1.20        L = .35
?DEV259638 MOS P  ----    Xi144-Xi1-Xi : ***** UN-MATCHED *****
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  8-Xi5-mM12                              :
 Xi144-Xi1-Xi8-i4_CO, DVDD,
 Xi144-Xi1-Xi8-Xi5-U21_OUT
    W = 3.00        L = .35
?DEV259616 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  8-Xi5-mM10                              :
 Xi144-Xi1-Xi8-Xi5-U27_IN2,
 ?Xi144-Xi1-Xi8-Xi5-U21_U7_DRAIN,
 Xi144-Xi1-Xi8-Xi5-U21_OUT
    W = 1.20        L = .35
?DEV259637 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  8-Xi5-mM11                              :
 Xi144-Xi1-Xi8-Xi5-U27_IN2, DVDD,
 Xi144-Xi1-Xi8-Xi5-U21_OUT
    W = 3.00        L = .35
OCCURRENCE NAME Xi144-Xi1-Xi8-Xi4-U21_OUT
?DEV296584 NAND                           :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi8-Xi4-U21_OUT,
 Xi144-Xi1-Xi8-i3_CO,
 Xi144-Xi1-Xi8-Xi4-U27_IN2
?DEV259659 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  8-Xi4-mM9                               :
 Xi144-Xi1-Xi8-i3_CO, DGND,
 ?Xi144-Xi1-Xi8-Xi4-U21_U7_DRAIN
    W = 1.20        L = .35
?DEV259682 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  8-Xi4-mM12                              :
 Xi144-Xi1-Xi8-i3_CO, DVDD,
 Xi144-Xi1-Xi8-Xi4-U21_OUT
    W = 3.00        L = .35
?DEV259660 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  8-Xi4-mM10                              :
 Xi144-Xi1-Xi8-Xi4-U27_IN2,
 ?Xi144-Xi1-Xi8-Xi4-U21_U7_DRAIN,
 Xi144-Xi1-Xi8-Xi4-U21_OUT
    W = 1.20        L = .35
?DEV259681 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  8-Xi4-mM11                              :
 Xi144-Xi1-Xi8-Xi4-U27_IN2, DVDD,
 Xi144-Xi1-Xi8-Xi4-U21_OUT
    W = 3.00        L = .35
OCCURRENCE NAME Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_OUT
?DEV296636 NAND                           :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
 OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
 , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
 7_IN2
?DEV265326 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi402-mM9                :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
 ALGND, ?Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi
 402-U21_U7_DRAIN
    W = 1.20        L = .35
?DEV265349 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi402-mM12               :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
 ALVDD, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi4
 02-U21_OUT
    W = 3.00        L = .35
?DEV265327 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
```



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  0-Xi1-Xi27-Xi0-Xi402-mM10               :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
 IN2, ?Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi40
 2-U21_U7_DRAIN, Xi144-Xi1-Xi0-Xi1-Xi2
 7-Xi0-Xi402-U21_OUT
    W = 1.20        L = .35
?DEV265348 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi402-mM11               :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
 IN2, ALVDD, Xi144-Xi1-Xi0-Xi1-Xi27-Xi
 0-Xi402-U21_OUT
    W = 3.00        L = .35
OCCURRENCE NAME Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OUT
?DEV296640 NAND                           :          ***** UN-MATCHED *****
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
 T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2
?DEV265414 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi5-mM9                  :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 ALGND, ?Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi
 5-U21_U7_DRAIN
    W = 1.20        L = .35
?DEV265437 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi5-mM12                 :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
 ALVDD, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5
 -U21_OUT
    W = 3.00        L = .35
?DEV265415 MOS N  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi5-mM10                 :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2, ?Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U2
 1_U7_DRAIN, Xi144-Xi1-Xi0-Xi1-Xi27-Xi
 0-Xi5-U21_OUT
    W = 1.20        L = .35
?DEV265436 MOS P  ----    Xi144-Xi1-Xi  :          ***** UN-MATCHED *****
  0-Xi1-Xi27-Xi0-Xi5-mM11                 :
 Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
 2, ALVDD, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-
 Xi5-U21_OUT
    W = 3.00        L = .35
1       ************************************************
        **********  UN-MATCHED LAYOUT DEVICES  **********
        **********     (LIST UP TO     100 )   **********
        ************************************************

                                  : ?DEV109742 MOS P
                                  :  X=1209.28      Y=-475.68
                                    ?94828, ?77951, ?77952
                                       W = 23.55        L = 5.00
                                  : ?DEV294753 SDW
                                  :  X=1797.10      Y=-1459.80
                                    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
                                    T, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
                                    Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
                                    2
                                  : ?DEV179203 MOS N
```

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: X=1797.95     Y=-1459.80
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U21_OU
  T, ?589849
     W = 1.20        L = .35
: ?DEV179202 MOS N
: X=1797.10     Y=-1459.80
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
  2, ALGND, ?589849
     W = 1.20        L = .35
: ?DEV294754 SDW
: X=1771.90     Y=-1434.80
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
  OUT, Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN
  , Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U2
  7_IN2
: ?DEV181504 MOS N
: X=1772.75     Y=-1434.80
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U21_
  OUT, ?589994
     W = 1.20        L = .35
: ?DEV181503 MOS N
: X=1771.90     Y=-1434.80
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
  IN2, ALGND, ?589994
     W = 1.20        L = .35
: ?DEV294765 SDW
: X=1047.30     Y=-1068.10
  Xi144-Xi1-Xi8-Xi7-U21_OUT,
  Xi144-Xi1-Xi8-i6_CO,
  Xi144-Xi1-Xi8-Xi7-U27_IN2
: ?DEV213945 MOS N
: X=1048.15     Y=-1068.10
  Xi144-Xi1-Xi8-i6_CO,
  Xi144-Xi1-Xi8-Xi7-U21_OUT, ?593222
     W = 1.20        L = .35
: ?DEV213944 MOS N
: X=1047.30     Y=-1068.10
  Xi144-Xi1-Xi8-Xi7-U27_IN2, DGND,
  ?593222
     W = 1.20        L = .35
: ?DEV294766 SDW
: X=1054.30     Y=-1043.10
  Xi144-Xi1-Xi8-Xi5-U21_OUT,
  Xi144-Xi1-Xi8-i4_CO,
  Xi144-Xi1-Xi8-Xi5-U27_IN2
: ?DEV215948 MOS N
: X=1055.15     Y=-1043.10
  Xi144-Xi1-Xi8-i4_CO,
  Xi144-Xi1-Xi8-Xi5-U21_OUT, ?593433
     W = 1.20        L = .35
: ?DEV215947 MOS N
: X=1054.30     Y=-1043.10
  Xi144-Xi1-Xi8-Xi5-U27_IN2, DGND,
  ?593433
     W = 1.20        L = .35
: ?DEV294767 SDW
: X=1097.70     Y=-1043.10
  Xi144-Xi1-Xi8-Xi4-U21_OUT,
```



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  Xi144-Xi1-Xi8-i3_CO,
  Xi144-Xi1-Xi8-Xi4-U27_IN2
: ?DEV215960 MOS N
: X=1098.55     Y=-1043.10
  Xi144-Xi1-Xi8-i3_CO,
  Xi144-Xi1-Xi8-Xi4-U21_OUT, ?593435
     W = 1.20        L = .35
: ?DEV215959 MOS N
: X=1097.70     Y=-1043.10
  Xi144-Xi1-Xi8-Xi4-U27_IN2, DGND,
  ?593435
     W = 1.20        L = .35
: ?DEV318343 PUP
: X=1797.20     Y=-1465.40
  ?36196,
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
  2
: ?DEV31659  MOS P
: X=1798.55     Y=-1465.40
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i4_CO,
  ALVDD, ?36196
     W = 3.00        L = .35
: ?DEV31658  MOS P
: X=1797.20     Y=-1465.40
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi5-U27_IN
  2, ALVDD, ?36196
     W = 3.00        L = .35
: ?DEV318374 PUP
: X=1772.00     Y=-1440.40
  ?37481,
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
  IN2
: ?DEV33981  MOS P
: X=1773.35     Y=-1440.40
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-i0_ZN,
  ALVDD, ?37481
     W = 3.00        L = .35
: ?DEV33980  MOS P
: X=1772.00     Y=-1440.40
  Xi144-Xi1-Xi0-Xi1-Xi27-Xi0-Xi402-U27_
  IN2, ALVDD, ?37481
     W = 3.00        L = .35
: ?DEV318894 PUP
: X=1047.40     Y=-1073.70
  ?54765, Xi144-Xi1-Xi8-i6_CO,
  Xi144-Xi1-Xi8-Xi7-U27_IN2
: ?DEV65596  MOS P
: X=1048.75     Y=-1073.70
  Xi144-Xi1-Xi8-i6_CO, DVDD, ?54765
     W = 3.00        L = .35
: ?DEV65595  MOS P
: X=1047.40     Y=-1073.70
  Xi144-Xi1-Xi8-Xi7-U27_IN2, DVDD,
  ?54765
     W = 3.00        L = .35
: ?DEV318913 PUP
: X=1054.40     Y=-1048.70
  ?55948, Xi144-Xi1-Xi8-i4_CO,
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   Xi144-Xi1-Xi8-Xi5-U27_IN2
: ?DEV67651  MOS P
:  X=1055.75      Y=-1048.70
   Xi144-Xi1-Xi8-i4_CO, DVDD, ?55948
      W = 3.00         L = .35
: ?DEV67650  MOS P
:  X=1054.40      Y=-1048.70
   Xi144-Xi1-Xi8-Xi5-U27_IN2, DVDD,
   ?55948
      W = 3.00         L = .35
: ?DEV318914 PUP
:  X=1097.80      Y=-1048.70
   ?55949, Xi144-Xi1-Xi8-i3_CO,
   Xi144-Xi1-Xi8-Xi4-U27_IN2
: ?DEV67653  MOS P
:  X=1099.15      Y=-1048.70
   Xi144-Xi1-Xi8-i3_CO, DVDD, ?55949
      W = 3.00         L = .35
: ?DEV67652  MOS P
:  X=1097.80      Y=-1048.70
   Xi144-Xi1-Xi8-Xi4-U27_IN2, DVDD,
   ?55949
      W = 3.00         L = .35
: ?DEV62      ?CAPPS
:  X=-427.75      Y=-162.80
   ?94829
: ?DEV63      ?CAPPS
:  X=-406.35      Y=-162.80
   ?94830
: ?DEV64      ?CAPPS
:  X=-384.95      Y=-162.80
   ?94831
: ?DEV65      ?CAPPS
:  X=-1392.00     Y=-162.70
   ?94832
: ?DEV66      ?CAPPS
:  X=-1382.30     Y=-162.70
   ?94833
: ?DEV67      ?CAPPS
:  X=-1372.60     Y=-162.70
   ?94834
: ?DEV68      ?CAPPS
:  X=-1166.45     Y=-162.70
   ?94835
: ?DEV69      ?CAPPS
:  X=-1155.70     Y=-162.70
   ?94836
: ?DEV70      ?CAPPS
:  X=-1144.95     Y=-162.70
   ?94837
: ?DEV71      ?CAPPS
:  X=-695.85      Y=-162.70
   ?94838
: ?DEV72      ?CAPPS
:  X=-678.55      Y=-162.70
   ?94839
: ?DEV73      ?CAPPS
:  X=-661.25      Y=-162.70
   ?94840
: ?DEV74      ?CAPPS
```



```
:  X=-944.85      Y=-162.65
   ?94841
: ?DEV75      ?CAPPS
:  X=-931.30      Y=-162.65
   ?94842
: ?DEV76      ?CAPPS
:  X=-917.75      Y=-162.65
   ?94843
: ?DEV81      ?CAPPS
:  X=-427.75      Y=-148.10
   ?94844
: ?DEV82      ?CAPPS
:  X=-406.35      Y=-148.10
   ?94845
: ?DEV83      ?CAPPS
:  X=-384.95      Y=-148.10
   ?94846
: ?DEV84      ?CAPPS
:  X=-105.55      Y=-148.10
   ?94847
: ?DEV85      ?CAPPS
:  X=-84.05       Y=-148.10
   ?94848
: ?DEV86      ?CAPPS
:  X=-62.55       Y=-148.10
   ?94849
: ?DEV87      ?CAPPS
:  X=-41.05       Y=-148.10
   ?94850
: ?DEV88      ?CAPPS
:  X=-1392.00     Y=-148.00
   ?94851
: ?DEV89      ?CAPPS
:  X=-1382.30     Y=-148.00
   ?94852
: ?DEV90      ?CAPPS
:  X=-1372.60     Y=-148.00
   ?94853
: ?DEV91      ?CAPPS
:  X=-1166.45     Y=-148.00
   ?94854
: ?DEV92      ?CAPPS
:  X=-1155.70     Y=-148.00
   ?94855
: ?DEV93      ?CAPPS
:  X=-1144.95     Y=-148.00
   ?94856
: ?DEV94      ?CAPPS
:  X=-695.85      Y=-148.00
   ?94857
: ?DEV95      ?CAPPS
:  X=-678.55      Y=-148.00
   ?94858
: ?DEV96      ?CAPPS
:  X=-661.25      Y=-148.00
   ?94859
: ?DEV97      ?CAPPS
:  X=-944.85      Y=-147.95
   ?94860
: ?DEV98      ?CAPPS
```

```
: X=-931.30     Y=-147.95
  ?94861
: ?DEV99     ?CAPPS
: X=-917.75     Y=-147.95
  ?94862
: ?DEV104    ?CAPPS
: X=-427.75     Y=-133.40
  ?94863
: ?DEV106    ?CAPPS
: X=-384.95     Y=-133.40
  ?94864
: ?DEV107    ?CAPPS
: X=-105.55     Y=-133.40
  ?94865
: ?DEV110    ?CAPPS
: X=-41.05      Y=-133.40
  ?94866
: ?DEV111    ?CAPPS
: X=-1392.00    Y=-133.30
  ?94867
: ?DEV113    ?CAPPS
: X=-1372.60    Y=-133.30
  ?94868
: ?DEV114    ?CAPPS
: X=-1166.45    Y=-133.30
  ?94869
: ?DEV116    ?CAPPS
: X=-1144.95    Y=-133.30
  ?94870
: ?DEV117    ?CAPPS
: X=-695.85     Y=-133.30
  ?94871
: ?DEV119    ?CAPPS
: X=-661.25     Y=-133.30
  ?94872
: ?DEV120    ?CAPPS
: X=-944.85     Y=-133.25
  ?94873
: ?DEV122    ?CAPPS
: X=-917.75     Y=-133.25
  ?94874
: ?DEV123    ?CAPPS
: X=-427.75     Y=-118.70
  ?94875
: ?DEV125    ?CAPPS
: X=-384.95     Y=-118.70
  ?94876
: ?DEV126    ?CAPPS
: X=-105.55     Y=-118.70
  ?94877
: ?DEV129    ?CAPPS
: X=-41.05      Y=-118.70
  ?94878
: ?DEV130    ?CAPPS
: X=-1392.00    Y=-118.60
  ?94879
: ?DEV132    ?CAPPS
: X=-1372.60    Y=-118.60
  ?94880
: ?DEV133    ?CAPPS
```



```
: X=-1166.45    Y=-118.60
  ?94881
: ?DEV135    ?CAPPS
: X=-1144.95    Y=-118.60
  ?94882
: ?DEV136    ?CAPPS
: X=-695.85     Y=-118.60
  ?94883
: ?DEV138    ?CAPPS
: X=-661.25     Y=-118.60
  ?94884
: ?DEV139    ?CAPPS
: X=-944.85     Y=-118.55
  ?94885
: ?DEV141    ?CAPPS
: X=-917.75     Y=-118.55
  ?94886
: ?DEV142    ?CAPPS
: X=-427.75     Y=-104.00
  ?94887
: ?DEV144    ?CAPPS
: X=-384.95     Y=-104.00
  ?94888
: ?DEV145    ?CAPPS
: X=-105.55     Y=-104.00
  ?94889
: ?DEV148    ?CAPPS
: X=-41.05      Y=-104.00
  ?94890
: ?DEV149    ?CAPPS
: X=-1392.00    Y=-103.90
  ?94891
: ?DEV151    ?CAPPS
: X=-1372.60    Y=-103.90
  ?94892
: ?DEV152    ?CAPPS
: X=-1166.45    Y=-103.90
  ?94893
: ?DEV154    ?CAPPS
: X=-1144.95    Y=-103.90
  ?94894
: ?DEV155    ?CAPPS
: X=-695.85     Y=-103.90
  ?94895
: ?DEV157    ?CAPPS
: X=-661.25     Y=-103.90
  ?94896
: ?DEV158    ?CAPPS
: X=-944.85     Y=-103.85
  ?94897

THE REST OF UN-MATCHED LAYOUT DEVICES ARE NOT LISTED

1       ************************************************
        **********     LVS SUMMARY (REPEATED)    **********
        ************************************************

        ************************************************
```

********** LVS DEVICE MATCH SUMMARY **********

NUMBER OF UN-MATCHED SCHEMATICS DEVICES = 5
NUMBER OF UN-MATCHED LAYOUT DEVICES = 359
NUMBER OF MATCHED SCHEMATICS DEVICES = 136208
NUMBER OF MATCHED LAYOUT DEVICES = 136208

********** DISCREPANCY POINTS SUMMARY **********

15 MATCHED NODE TO UN-MATCHED LAYOUT AND SCHEMATIC DEVICES

******** DEVICE MATCHING SUMMARY BY TYPE ********

TYPE	SUB-TYPE	TOTAL DEVICE SCH.	TOTAL DEVICE LAY.	UN-MATCHED DEVICE SCH.	UN-MATCHED DEVICE LAY.
MOS	P	122675	122676	10	11
MOS	N	123029	123029	10	10
BJT	SP	168	168	0	0
RES	P	247	247	0	0
RES	M1	1	1	0	0
RES	N	403	403	0	0
RES	M3	6	6	0	0
RES	NW	9	9	0	0
RES	P1	12	12	0	0
RES	P2	4	4	0	0
RES	B1	24	24	0	0
DIO	ND	96	96	0	0
CAP	N	21	21	0	0
CAP	PW	32	32	0	0
CAP	PS	170	170	0	0

**/W* -- SCHEMATIC AND LAYOUT MAY NOT MATCH **
** CHECK ALL ABOVE DISCREPANCY **
** AND WARNING MESSAGES **

Statistics Of Layers

Layer #	Rectangles	Polygons	Paths	Ellipses	Labels	Nodes
0	10	1	13	0	247	0
1	7351	140	12	0	0	0
2	7889	461	12	0	0	0
4	6546	1693	40	0	0	0
5	10831	499	5	0	0	0
7	147304	0	3	0	0	0
8	31753	2388	11513	0	0	0
9	140836	0	2	0	0	0
10	7280	228	98420	0	1	0
11	1	0	1	0	0	0
12	77	13	0	0	0	0
13	2	0	0	0	0	0
14	123135	0	3	0	0	0
15	1158	113	53811	0	1	0
19	11	0	0	0	0	0
21	0	0	0	0	2455	0
22	0	0	0	0	2413	0
24	0	0	0	0	1945	0
25	134	2	0	0	0	0
33	147	23	0	0	1	0
38	50739	15317	0	0	0	0
40	3448	1354	0	0	0	0
43	6633	86	0	0	0	0
44	3448	1354	0	0	0	0
56	244	0	0	0	0	0
57	15788	2500	0	0	0	0
58	15549	2585	1	0	0	0
59	137044	57314	1	0	0	0
63	9	2	52	0	7775	0
64	154	87	0	0	0	0
235	9	6	0	0	0	0

LVS LVS.

LVSS	LVS with Stamps.
DRC	Drc's only Standard Pitch Pads
DRCFP	Drc's only Fine Pitch Pads
MDRC	Manufacturing drcs.
ERC	ERC(Ercs only)
ESDLAT	Esd and Latchup checks.
GBCHK	Latchup Guardband/contacting checks.
OPSHRTS	OPSHRTS (Opens/Shorts only)
DLVSS	DRC's and LVS with Stamps.
DERC	Drc and ERC
DRCF	Drc's with gen.layers output.
ALLDRC	All Drc's for QA.
ANTENNA	Antennae checks.
XOR1	Xor of two layers on an 140_dptm strm file.
XOR2	Xor.<140_dptm stream file A> to <140_dptm strm. file B>
GENLYR	Generate output layers only.
LPEI	LPE.(Parasitic caps only) lumped to gnd.
LPEIG	LPE.(Par. + MOS gate caps) lumped to gnd.
LPEIGSD	LPE.(Par.+ MOS gate+sdrn. caps) lumped to gnd.
LPEC	LPE.(Parasitics) Cross-coupled inc. to Gnd.
PNCHK	Drcs on placed gen.layers.
PNCHKFP.	Drcs (Fine Pitch) on placed gen.layers.
PNLVS.	LVS on placed gen.layers.

Input job type = [genlyr]

X -2780 X 2780

Y -2565 Y 2565

[redacted]

[redacted]

9887A

74434560 size file

nonact

seal_ring

Job Parameters

(0)	system	[GDSII]
(1)	Master Sublock <schematic>	[M9887ac . 1]
(2)	Cdl Filename	[9887ace] .cdl]
(3)	Primary Structure	[9887A]
(4)	Stream file name	[9887a.gds]
(5)	Output Stream file name	[9887a.gds]
(6)	Power Nodes	[ALVDD AVDD DVDD OVDD PVDD]
(7)	Ground Nodes	[AGND AVSS DGND OGND PGND SUBGND]
(8)	Keep Temporary data <YES/NO/SMART>	[YES]
(9)	Device Size Checking	[YES]
(10)	Optional extra parameters	
(11)	Save Project file	
(12)	Help	
(13)	Abort program	
(14)	Exit	

Enter number <0-14>:-